SPECIFICATIONS

CUSTOMER	:	
SAMPLE CODE	:	SRA800480T033IHC07
MASS PRODUCTION CODE	:	HRA800480T033IHC07
SAMPLE VERSION	:	01
SPECIFICATIONS EDITION	:	002
DRAWING NO. (Ver.)	:	LMD-HRA800480T033IHC07 (Ver.002)
PACKAGING NO. (Ver.)	:	PKG-HRA800480T033IHC07 (Ver.001)

Customer Approved

Date:

Approved	Checked	Designer
林裘中 Daniel Lin	呂清溪 Marcs Lu	王兆以 Hank Wang

POWERTIP
2022.01.17
TW RD APR

☐ Preliminary specification for design input

■ Specification for sample approval

POWERTIP TECH. CORP.

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www.first-components.com
technical@first-components.com
+39 0332 1888005

1. SPECIFICATIONS

1.1 Features

Hardware

CPU	RISC Processor	PX30 Quad-Core ARM Cortex-A35
Memory	On Board RAM	2GB DDR3L SDRAM
	On Board ROM	8GB eMMC Flash
	External Storage	1 × Micro SD (max. 32G)
Display	Resolution	800 × 480 (RGB) DOTS
	LCD Type	a-Si TFT, Normally white, Transmissive type
	Touch type	Capacitive Touch Panel
	Interface	24 Bits RGB interface
I/O	USB	1 × USB OTG 2 × USB1.1/2.0 Compliant Host
	Ethernet	10/100 Mbps
	Analog Audio	1 × Output 1 × Input
	Wi-Fi	IEEE 802.11 a/b/g/n/ac
	Bluetooth	4.2
	Display	1 × RGB
	Serial	6 × UART (One for Debug Used) 3 × I2C 1 × SPI
Power Input	DC	9V ~ 24V

Software

OS	Embedded	Debian 9 (Linux kernel 4.4.194)

1.2 Mechanical Specifications

Item	Standard Value	Unit
Outline Dimension (PCBA)	118.10(W) x 87.0(L) x 25.0(H) MAX	mm
Outline Dimension (Display)	131.0(W) x 90.5(L) x 4.99(H) MAX	mm
Active Area (Display)	108.0 (W) x 64.8(L)	mm

1.3 Absolute Maximum Ratings

Ta = 25℃

Item	Symbol	Condition	Min.	Max.	Unit
Power Supply	V_{IN}	—	-0.3	26.0	V
Operating Temperature (T_s)	T_{OP}	—	0	70	℃
Storage Temperature (T_a)	T_{OP}	—	-30	80	℃
Humidity	H_D	T_a=60 ℃	10	90	%RH

The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the product may be permanently destroyed.

Note 1: T_s is the temperature of panel's surface,

Note 2: T_a is the ambient temperature of samples.

1.4 DC Electrical Characteristics

Ta = 25℃

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply Voltage	V_{IN}	-	9	12	24	V
Power Supply Current	I_{IN1}	V_{IN} = 12V	-	0.26	-	A
IO High-Level input voltage	V_{IH}	-	0.7xV_{DD3V3}	-	V_{DD3V3+}0.3	V
IO Low-Level input voltage	V_{IL}	-	-0.3	-	0.3xV_{DD3V3}	V
IO High-Level output voltage	V_{OH}	-	-	-	3.6	V
IO Low-Level output voltage	V_{OL}	-	-0.3	-	-	V

Note 1: V_{IN} is connected to 'J4' connector.

1.5 Optical Characteristics

Ta = 25℃

Item		Symbol	Condition	Min.	Typ.	Max.	unit	-
Response time	Tr+Tf	25℃	-	-	30	-	ms	-
Viewing angle	Top	θY+	CR ≥ 10	-	60	-	Deg.	Note 4
	Bottom	θY-		-	60	-		
	Left	θX-		-	60	-		
	Right	θX+		-	60	-		
Contrast ratio		CR	(Ta = 25℃) θX, θY = 0°	500	600	-	-	Note 3
Color of CIE Coordinate （With B/L & T/P）	White	X		0.24	0.29	0.34	-	Note1
		Y		0.26	0.31	0.36		
	Red	X		0.51	0.56	0.61		
		Y		0.28	0.33	0.38		
	Green	X		0.29	0.34	0.39		
		Y		0.54	0.59	0.64		
	Blue	X		0.08	0.13	0.18		
		Y		0.03	0.08	0.13		
Average Brightness Pattern=white display (With T/P)*1		IV	-	680	850	-	cd/m2	Note1
Uniformity (With T/P)*2		△ B		70	-	-	%	Note1

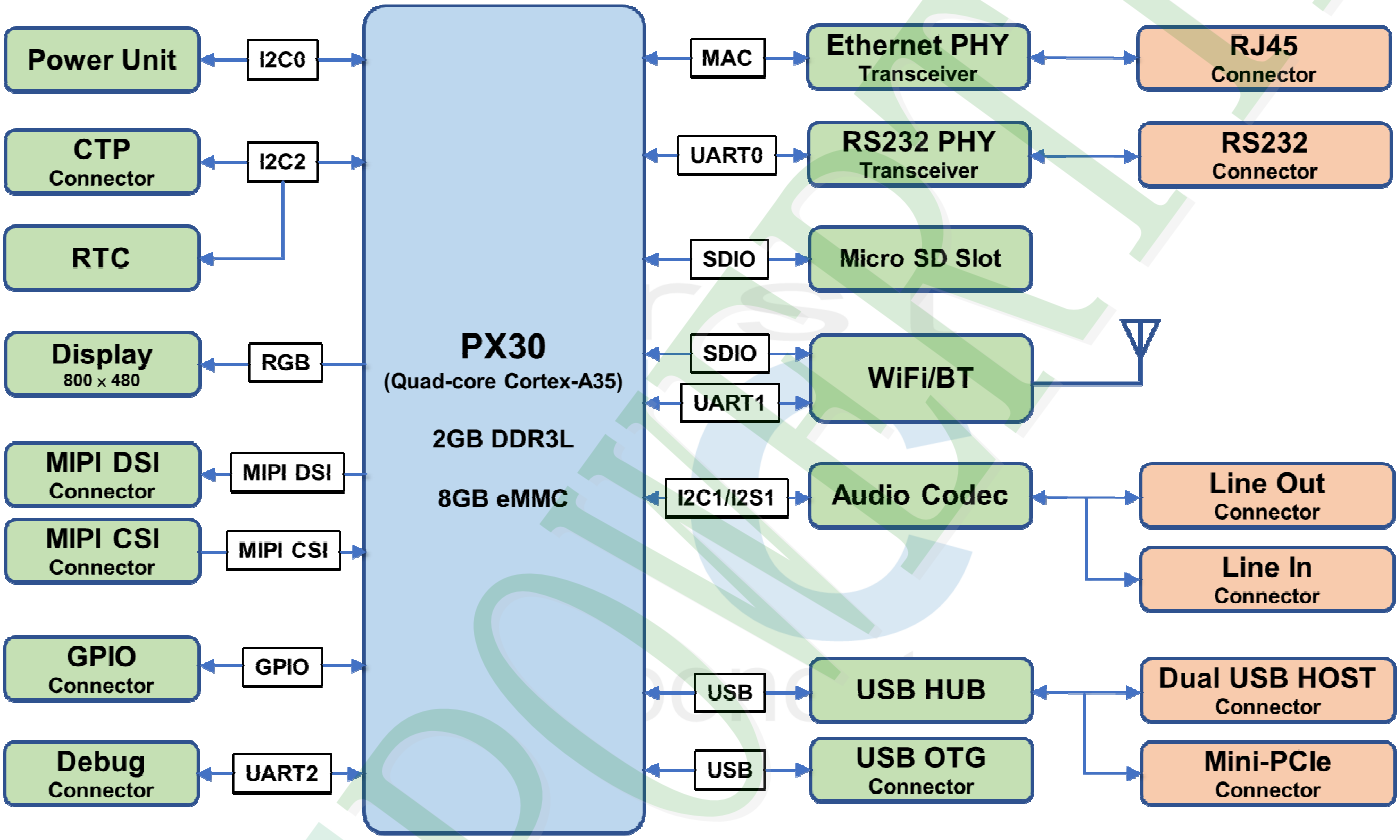
2. MODULE STRUCTURE

2.1 Counter Drawing

2.1.1 LCM Mechanical Diagram

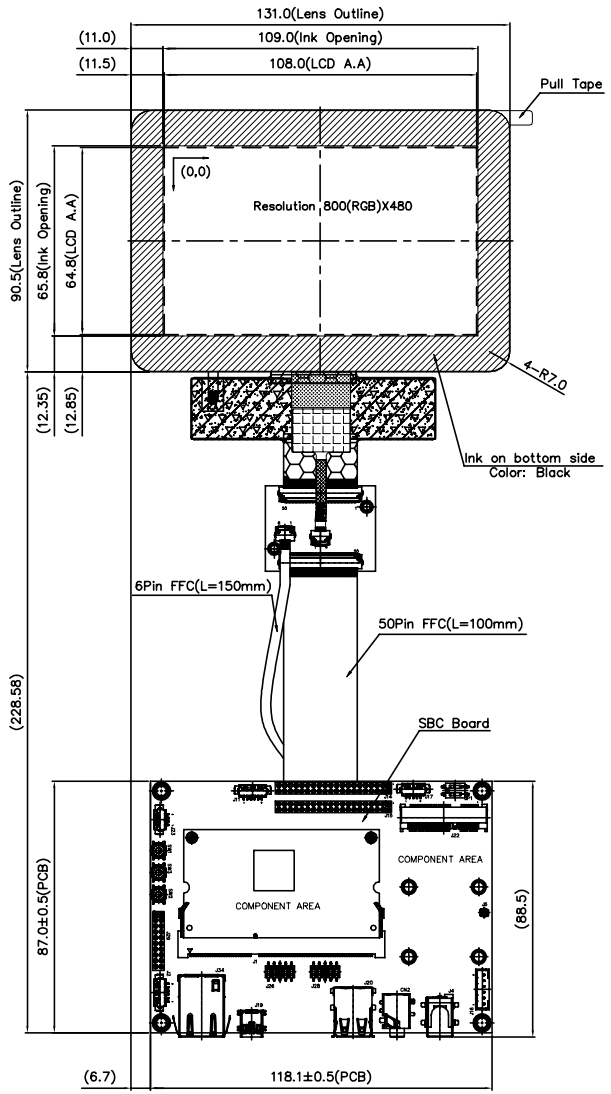
* See Appendix

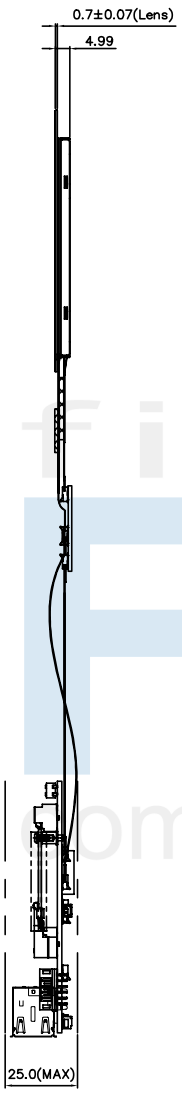
2.1.2 Block Diagram

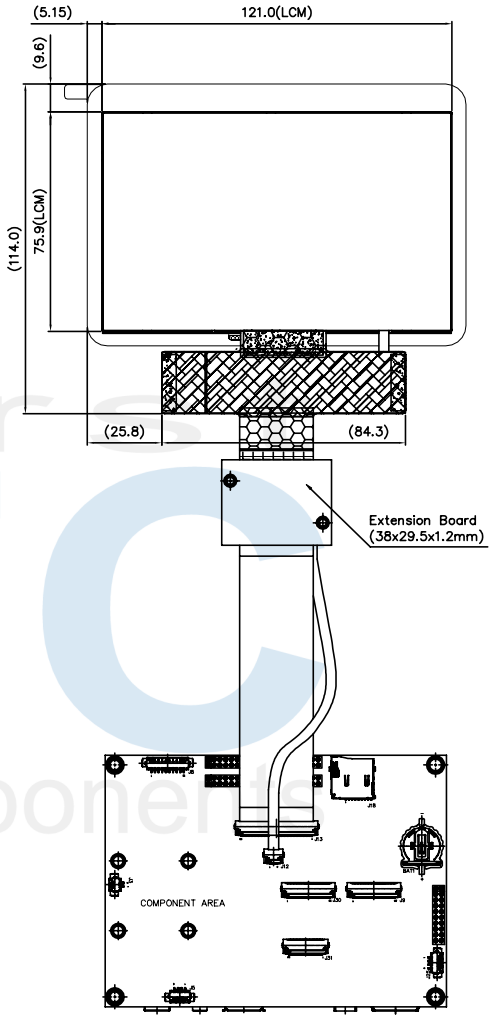


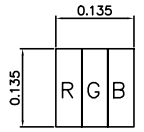
2.2 Interface Pins

Symbol	DESCRIPTION	NOTE
J1	SODIMM 204 PIN	
J2	Boot Mode Control	
J3	RTC Power Supply (Pitch 1.25mm)	
J4	System Power Supply	
J5	Audio Signal Input (Pitch 1.25mm)	
J6	Antenna connector	
J7	HDMI	No function, not used
J8	Expansion	
J9	Pitch 0.5mm 30pin connector bottom contact)	
J11	CTP Interface (Pitch 1.25mm 6pin)	
J12	CTP Interface (Pitch 0.5mm 6pin connector bottom contact)	
J13	RGB Interface (Pitch 0.5mm 50pin connector bottom contact)	
J14	LVDS, MIPI DSI Interface (Pitch 2.0mm, 2x20pin)	
J15	RGB Interface (Pitch 2.0mm, 2x20pin)	
J16	Backlight Power (Pitch 2.0mm 6pin)	
J17	USB CTP	
J18	MicroSD Card	
J19	USB 2.0 Device Mini USB	
J20	USB 1.1/2.0 Host USB Port A Type	
J22	Mini PCIe	
J23	Power Output (Pitch 1.25mm)	
J25	Debug (Pitch 1.25mm, 4pin)	
J26	RS232 (Pitch 2.0mm, 2x5pin)	
J27	UART0	No function, not used
J29	GPIO (Pitch 2.0mm, 2x10pin)	
J30	MIPI CSI (Pitch 0.5mm 30pin connector bottom contact)	
J31	Pitch 0.5mm 24pin connector bottom contact	
J34	Ethernet RJ45	
BAT1	Battery Slot (CR1220)	
JP1	Boot Mode Switch	
CN2	Audio Signal Output (Standard Earphone Jack ψ3.5mm)	
SW1	Reset System	
SW2	PMIC Power On	
SW3	ADC2 Key In	









DOTS DETAIL
SCALE: 200X

NOTES:
1. LCD TYPE: TFT LCD
2. LCD DISPLAY: POSITIVE/TRANSMISSIVE
3. VIEW DIRECTION: 6 O'CLOCK
4. The tolerance unless classified ±0.3mm

REV	REV BY	REVISER	DATE
007			
006			
005			
004			
003			
002	REMOVE J7、J10、J27	Nini	2021/12/03
001	NEW DRAWING	Nini	2021/04/12

PART NO: HRA800480T033IHC07

DRAWING NAME : LMD-HRA800480T033IHC07

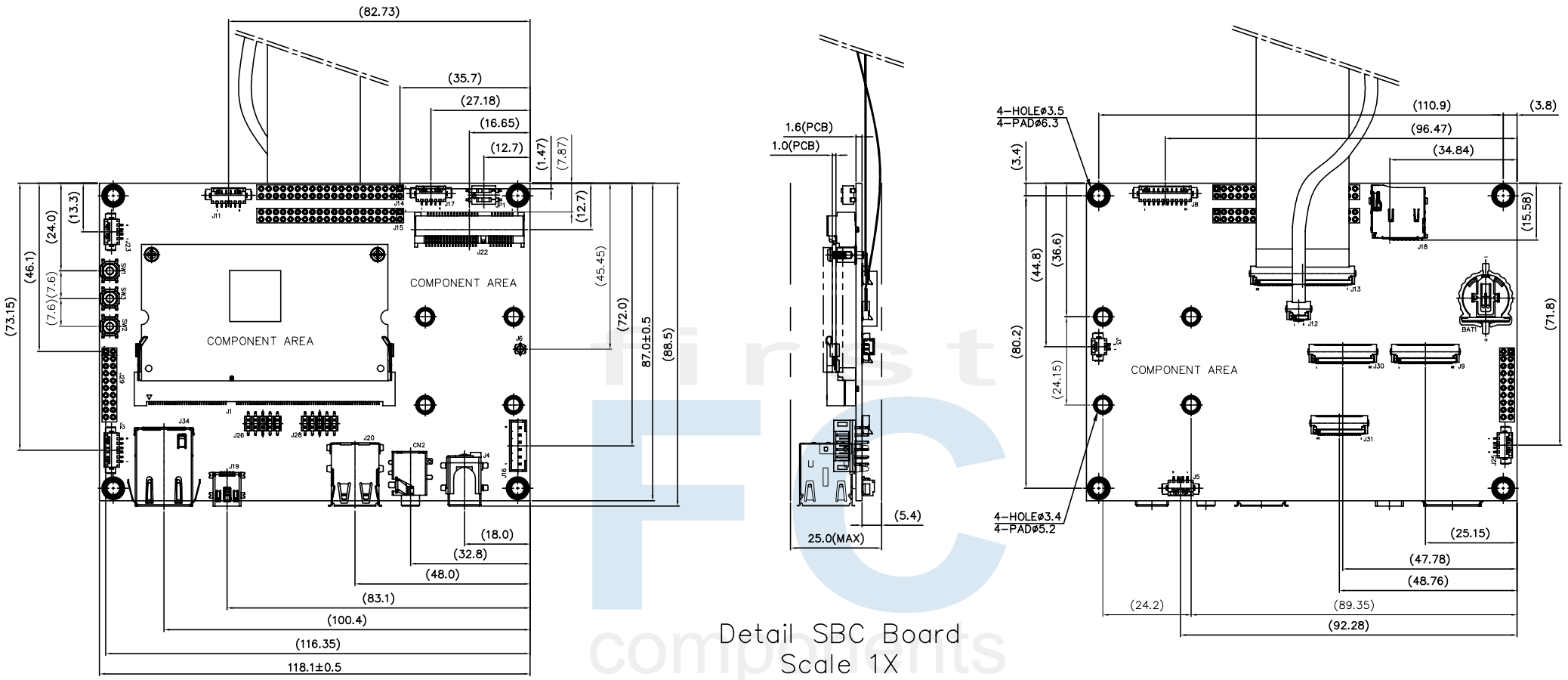
TITLE: LCD MODULE DRAWING

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Design	Nini Chen	(3)	Surface		
Check	Marcs Lu	Unit	MM	Material	
Approve	Daniel Lin	Scale	FIT	Thickness	
		Page	1/2	Quantity	

Length(Min) (mm) \ Tolerance \ Precision	Level
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

first components



Detail SBC Board
Scale 1X

NOTES:

5.J2 ,J11: Molex 532610671 or Compatible
6.J3: Molex 532610271 or Compatible
7.J5 ,J23 ,J25: Molex 532610471 or Compatible
8.J8: Molex 532611071 or Compatible
9.J9 ,J30: OMRON XF2M-3015-1A or Compatible
10.J16: Molex 894000610 or Compatible
11.J17: Molex 532610571 or Compatible
12.J31: OMRON XF2M-2415-1A or Compatiblele

REV	REV BY	REVISER	DATE
007			
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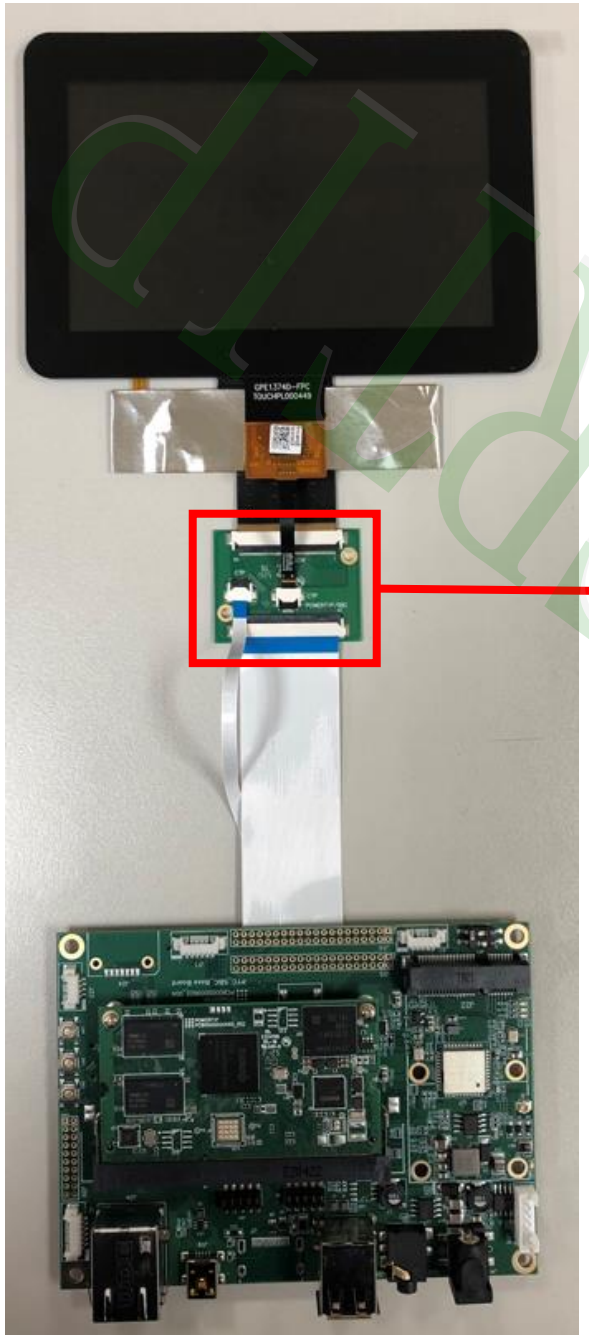
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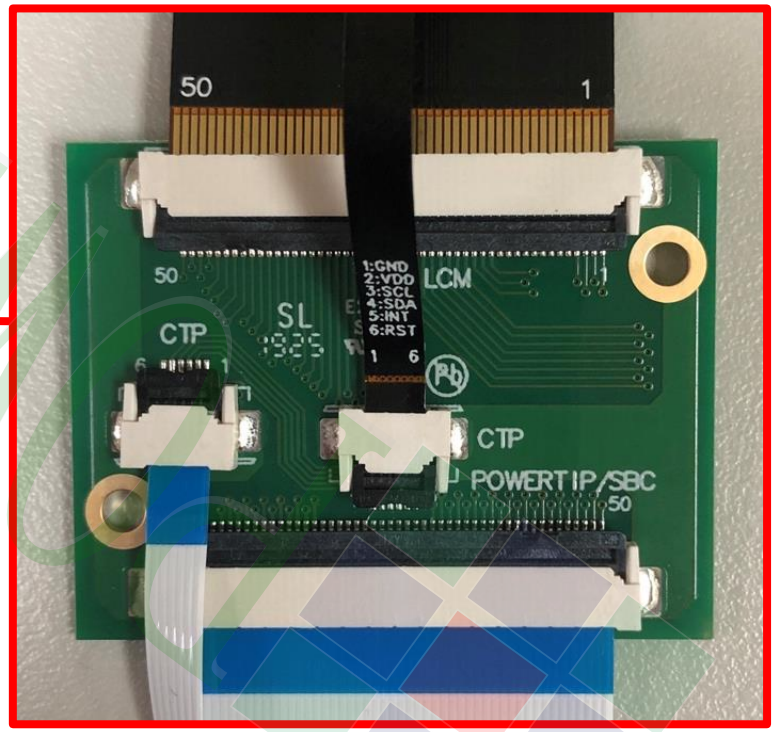
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Design	Nini Chen	(3)	Surface		
Check	Marcs Lu	Unit	MM	Material	
Approve	Daniel Lin	Scale	FIT	Thickness	
		Page	2/2	Quantity	

Length(Min) (mm) \ Tolerance \ Precision	Level
1 ~ 4	-
4 ~ 16	-
16 ~ 63	-
63 ~ 250	-
250 ~ 1000	-

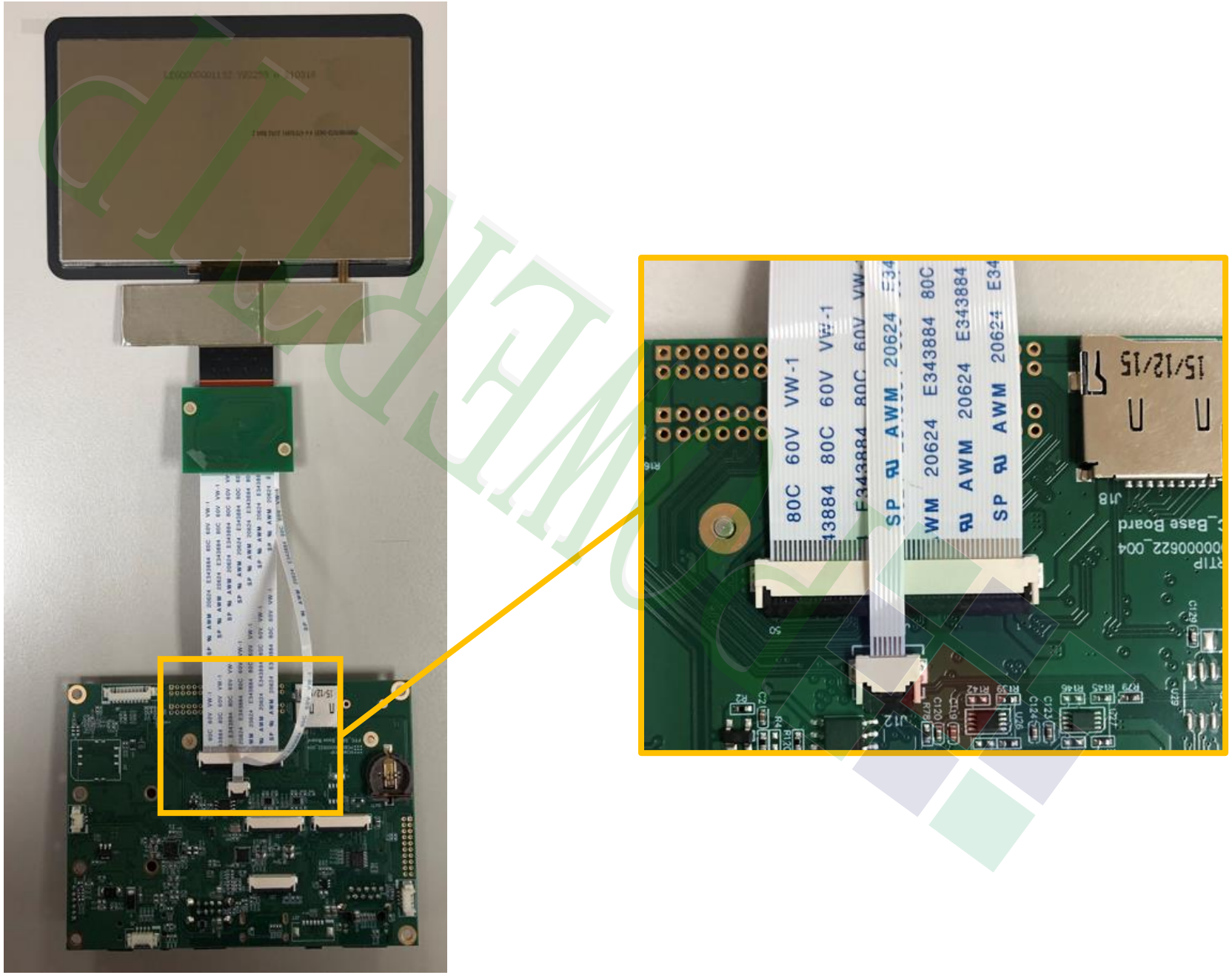
HRA800480T033IHC07 Assembly Guide(TOP)





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HRA800480T033IHC07 Assembly Guide(BOTTOM)



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