SMART DISPLAY SPECIFICATION



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SPECIFICATION

MODEL NO. : WL0F00101000JGAADSA00-FC

Summary

10.1 Inch Smart Display (CANopen series) Features

1. +12V power supply input, the power consumption is around 6W.
2. Self testing after booting function.
3. CAN bus communication interface.
4. Supports CANopen protocol. Default baud rate is at 250KB.
5. Built in 16MB flash memory, store the font and Object Dictionary Data.
6. Support capacitive touch panel (CTP).
7. Embedded buzzer controlled by Master Device.
8. Demo set HOST can be used on multiple platforms, such as Computer (with USB to CAN Dongle), MCU, Raspberry Pi (with PiCAN2).

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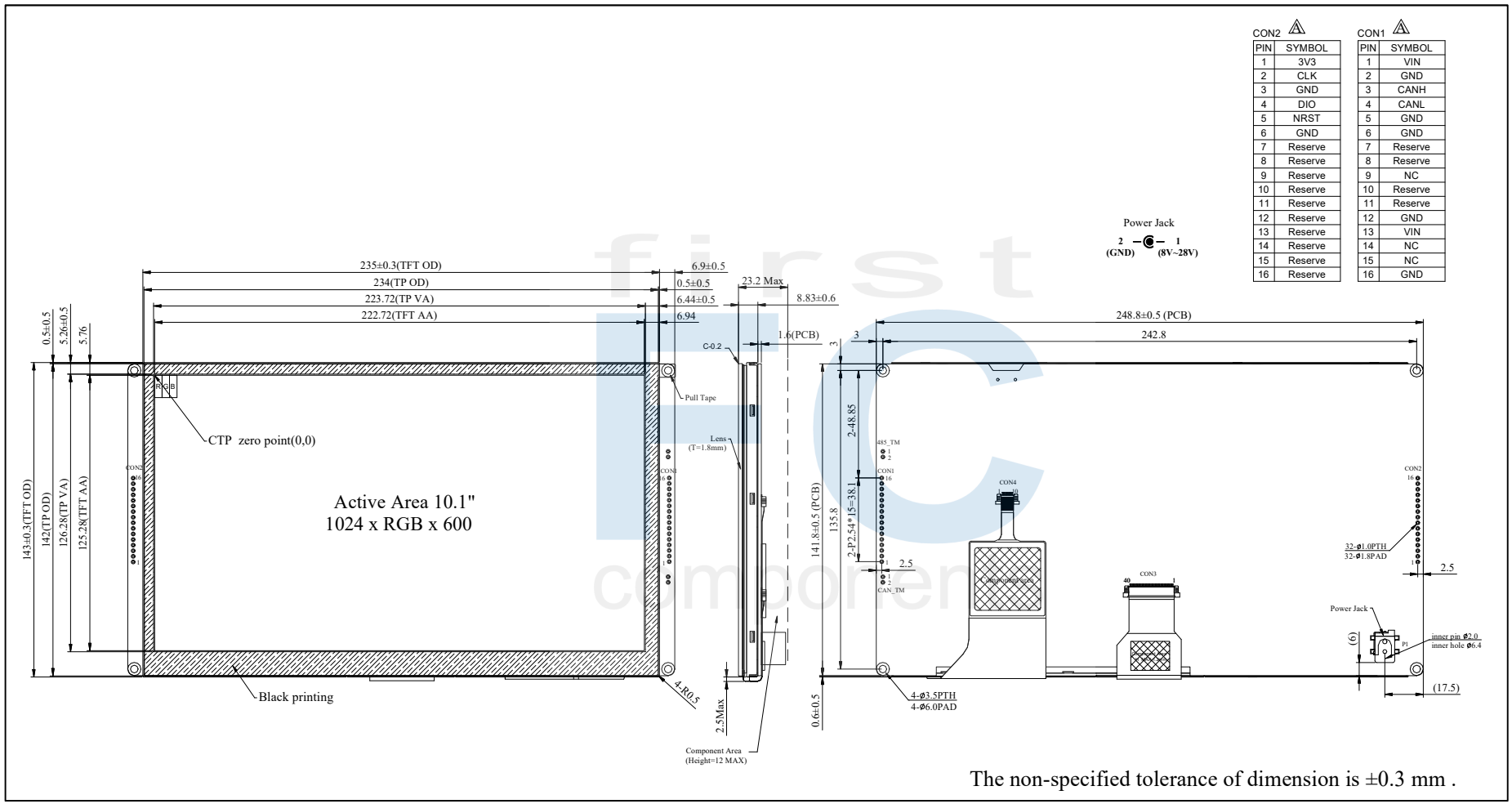
Product information

General information

Item	Standard Value	Unit
Operating voltage	8V~28V dynamic	Vdc
Communication Interface	CAN bus	Vpp
MCU	STM32F746	N/A
Flash Memory	16	MB
LCD display size	10.1	inch
Dot Matrix	1024 x RGB x 600(TFT)	dot
Module dimension	248.8 (W) x 143(H) x 23.2(D)	mm
Active area	222.72 (H) x 125.28(V)	mm
Pixel pitch	0.2175(W) x 0.2088(H)	mm
Brightness	Min:300; Typ:400	
LCD type	TFT, Normally Black, Transmissive	
View Direction	85/85/85/85	
Aspect Ratio	16:9	
Touch Panel	With CTP	
Surface	Glare	

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Contour Drawing



Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage	VCC	—	8	12	28	V
Supply LCM current	I(mA)	-	-	550	-	mA

BOM

Item	Description
LCM	WF101-1024600JL#0P8-FC
PCBA	SV10010R100JC00N0100

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Interface

CON1 definition:

Pin	Symbol	Function	Remark
1	VIN	Power supply 8V to 28V input	Input
2	GND	Power supply GND input	Input
3	CANH	CAN bus D+	I/O
4	CANL	CAN bus D-	I/O
5	GND	Power supply GND input	GND
6	GND	Power supply GND input	GND
7	Reserve	USB_D-	Reserve
8	Reserve	USB_D+	Reserve
9	NC	-	-
10	Reserve	USART RX interface(Reserve)	Reserve
11	Reserve	USART TX interface(Reserve)	Reserve
12	GND	Power supply GND input	GND
13	VIN	Power supply 8V to 28V input	Input
14	NC	-	-
15	NC	-	-
16	GND	Power supply GND input	Input

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CON2 definition:

Pin	Symbol	Function	Remark
1	3V3	3.3V power for JTAG interface	Output
2	CLK	CLK pin for JTAG interface	Input
3	GND	GND for JTAG interface	Output
4	DIO	Data pin for JTAG interface	I/O
5	NRST	Reset pin for JTAG interface	Input
6	GND	GND	Output
7	Reserve	(PA0) for system Resume from suspend (Reserve)	WKup,ADC,Timer,Event,I/O
8	Reserve	Reset (active Low)	I
9	Reserve	IO_0	ADC,DAC,Timer,Event,I/O
10	Reserve	IO_1	ADC,Timer,Event,I/O
11	Reserve	IO_2	ADC,Timer,Event,I/O
12	Reserve	IO_3	RST,Timer,Event,I/O
13	Reserve	IO_4	RST,Timer,Event,I/O
14	Reserve	IO_5	ADC,Timer,Event,I/O
15	Reserve	IO_6	RST,Timer,Event,I/O
16	Reserve	IO_7	RST,ADC,Event,I/O

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