Winstar Display Co., LTD
華凌光電股份有限公司
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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WH2004G1-TGH-EW#

General Specification

Item	Dimension	Unit
Number of Characters	20 characters x 4Lines	—
Module dimension	87.0 x 58.0 x 13.6 (MAX)	mm
View area	74.4 x 24.8	mm
Active area	70.4 x 20.8	mm
Dot size	0.55 x 0.55	mm
Dot pitch	0.60 x 0.60	mm
Character size	2.95 x 4.75	mm
Character pitch	3.55 x 5.35	mm
LCD type	STN Positive, Gray Transflective (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty	1/16	
View direction	6 o'clock	
Backlight Type	LED, White	

first FC components

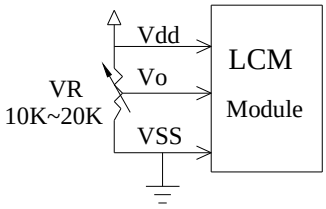
Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Input Voltage	V_{IN}	-0.3	—	V_{DD}+0.3	V
Supply Voltage For Logic	V_{DD}-V_{SS}	-0.3	—	5.5	V
Supply Voltage For LCD	V_{DD}-V_0	V_{SS} -0.3	—	V_{SS} +7.0	V

Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	V_{DD}-V_{SS}	—	4.5	5.0	5.5	V
Supply Voltage For LCD *Note	V_{DD}-V_0	Ta=-20℃ Ta=25℃ Ta=70℃	— 3.9 —	— 4.2 —	— 4.5 —	V V V
Input High Volt.	V_{IH}	—	2.5	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	-0.3	—	0.55	V
Output High Volt.	V_{OH}	—	3.9	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	—	—	0.4	V
Supply Current	I_{DD}	V_{DD}=5.0V	1.0	1.2	1.5	mA

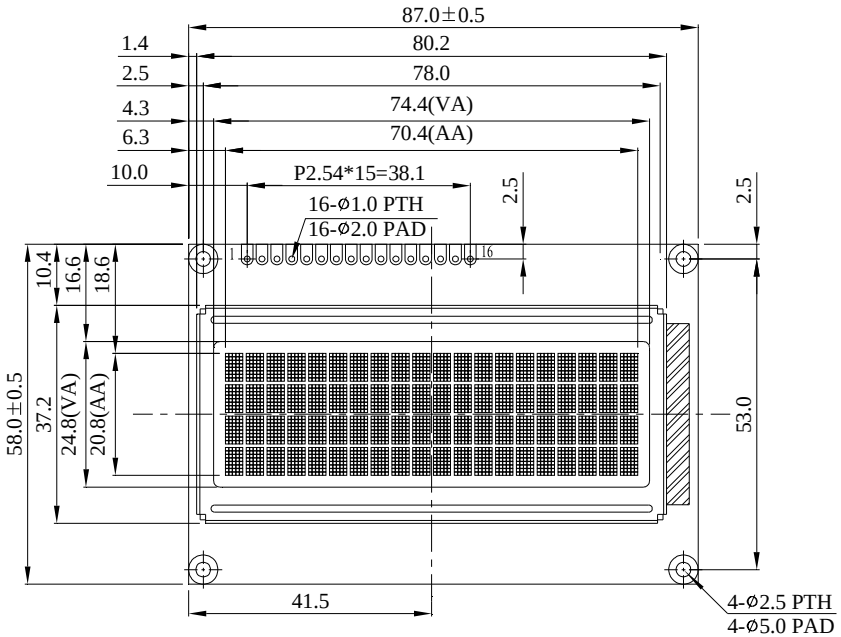
* Note: Please design the VOP adjustment circuit on customer's main board

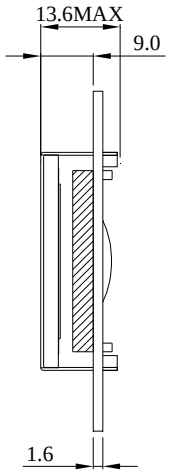


Interface Pin Function

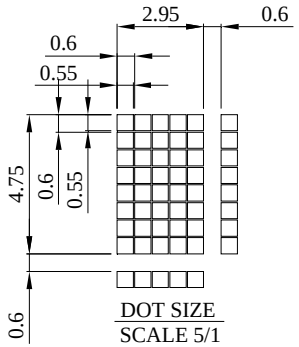
Pin No.	Symbol	Level	Description
1	V_{SS}	0V	Ground
2	V_{DD}	5.0V	Supply Voltage for logic
3	VO	(Variable)	Operating voltage for LCD
4	RS	—	In bus mode, used as register selection input. When RS = “High”, Date register is selected. When RS = “Low”, Instruction register is selected.
5	NC	—	No connection
6	NC	—	No connection
7	NC	—	No connection
8	NC	—	No connection
9	NC	—	No connection
10	NC	—	No connection
11	NC	—	No connection
12	CSB	H/L	In 4-SPI serial mode, used as chip selection input. When CSB = “Low”, selected When CSB = “High”, not selected. (Low access enable)
13	SCLK	H/L	Serial clock input
14	SID	H/L	Serial data input
15	A	—	Power supply for B/L +
16	K	—	Power supply for B/L -

Contour Drawing &Block Diagram



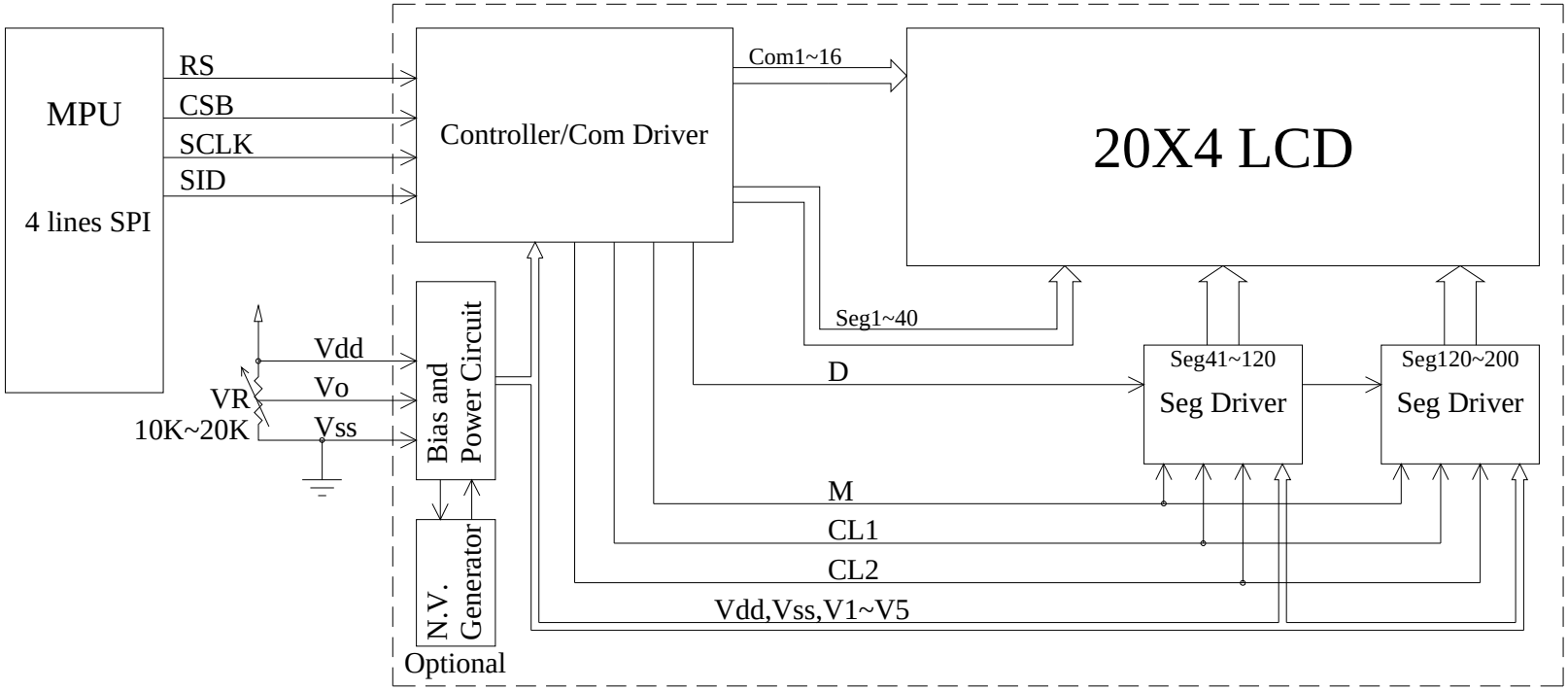


PIN NO.	SYMBOL
1	Vss
2	Vdd
3	Vo
4	RS
5	NC
6	NC
7	NC
8	NC
9	NC
10	NC
11	NC
12	CSB
13	SCLK
14	SID
15	A
16	K



The non-specified tolerance of dimension is ±0.2mm.

first FC components



External contrast adjustment.

Character located	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	18	19	20
DDRAM address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F	10	11	12	13
DDRAM address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F	50	51	52	53
DDRAM address	14	15	16	17	18	19	1A	1B	1C	1D	1E	1F	20	21	22	23	24	25	26	27
DDRAM address	54	55	56	57	58	59	5A	5B	5C	5D	5E	5F	60	61	62	63	64	65	66	67