Next Vision Display
Brighten The Future

NEXT VISION DISPLAY

SPECIFICATION

CUSTOMER : _____

MODULE NO.: NH1602BN002WS

Version	D
Engineer	
Date	2022/11/11

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Customer:
Approved by

NEXT VISION DISPLAY CO., LTD.

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Represented by:

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FIRST Components s.r.l.
www.first-components.com
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General Specification

Item	Dimension	Unit
Number of Characters	16 characters x 2Lines	—
Module dimension	122.0 x 44.0 x 13.6(MAX)	mm
View area	99.0 x 24.0	mm
Active area	94.2 x 20.0	mm
Dot size	0.92 x 1.1	mm
Dot pitch	0.97 x 1.16	mm
Character size	4.8 x 8.06	mm
Character pitch	5.96 x 9.66	mm
LCD type	STN Positive, Gray Transflective (In LCD production, It will occur slightly color difference. We can only guarantee the same color in the same batch.)	
Duty	1/16	
View direction	6 o'clock	
Backlight Type	LED, White	
IC	ST7066U	

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Absolute Maximum Ratings

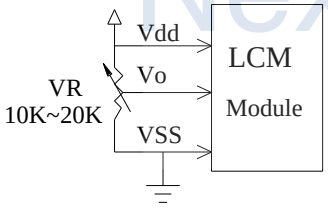
Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	T_{OP}	-20	—	+70	℃
Storage Temperature	T_{ST}	-30	—	+80	℃
Input Voltage	V_I	V_{SS}	—	V_{DD}	V
Supply Voltage For Logic	V_{DD}-V_{SS}	-0.3	—	7	V
Supply Voltage For LCD	V_{DD}-V_o	-0.3	—	13	V

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Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage For Logic	V_{DD}-V_{SS}	—	4.5	5.0	5.5	V
Supply Voltage For LCD *Note	V_{DD}-V_0	Ta=-20℃	—	—	5.2	V
		Ta=25℃	4.1	4.2	4.3	V
		Ta=70℃	3.2	—	—	V
Input High Volt.	V_{IH}	—	0.7 V_{DD}	—	V_{DD}	V
Input Low Volt.	V_{IL}	—	Vss	—	0.6	V
Output High Volt.	V_{OH}	—	3.9	—	V_{DD}	V
Output Low Volt.	V_{OL}	—	0	—	0.4	V
Supply Current	I_{DD}	V_{DD}=5.0V	1.2	1.5	1.8	mA

* Note: Please design the VOP adjustment circuit on customer's main board

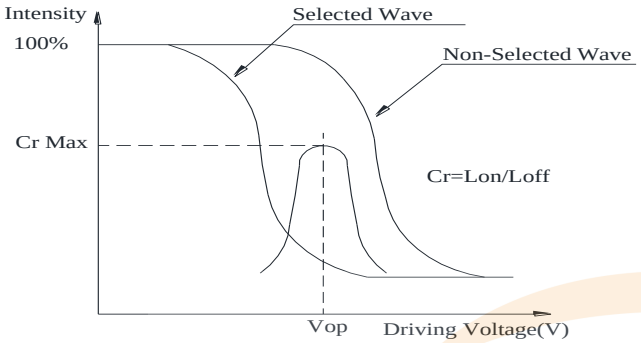


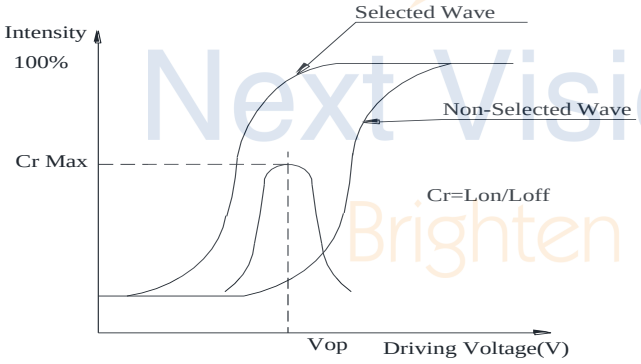
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Optical Characteristics

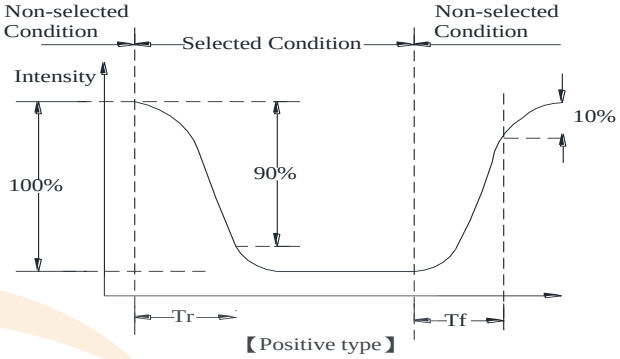
Item	Symbol	Condition	Min	Typ	Max	Unit
View Angle	θ	CR≧2	0	—	20	ψ = 180°
	θ	CR≧2	0	—	40	ψ = 0°
	θ	CR≧2	0	—	30	ψ = 90°
	θ	CR≧2	0	—	30	ψ = 270°
Contrast Ratio	CR	—	—	3	—	—
Response Time	T rise	—	—	150	200	ms
	T fall	—	—	150	200	ms

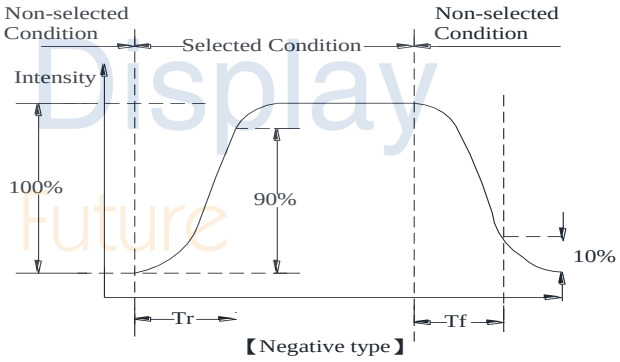
Definition of Operation Voltage (Vop)





Definition of Response Time (Tr , Tf)





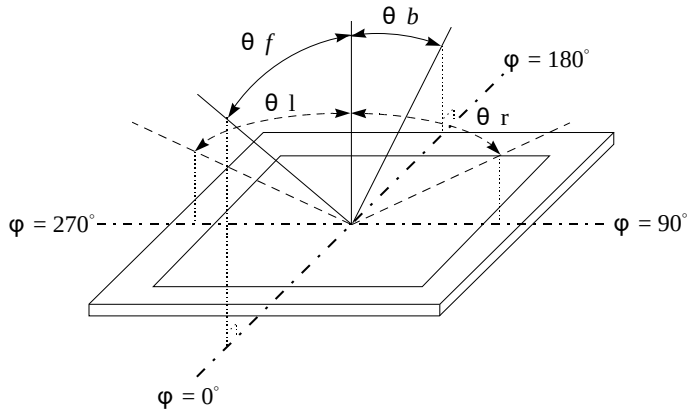
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Conditions :

Operating Voltage : Vop　　Viewing Angle(θ ，φ) : 0°， 0°

Frame Frequency : 64 HZ　　Driving Waveform : 1/N duty , 1/a bias

Definition of viewing angle(CR≧2)

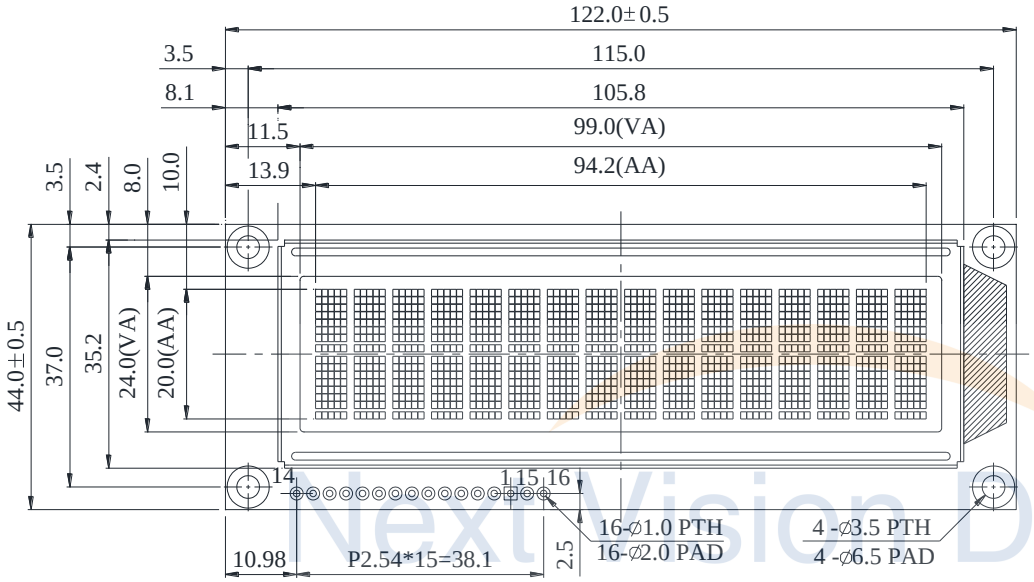


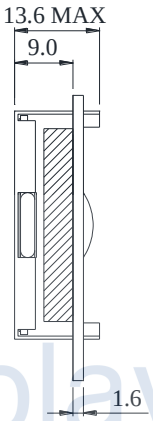
Interface Pin Function

Pin No.	Symbol	Level	Description
1	V_{SS}	0V	Ground
2	V_{DD}	5.0V	Supply Voltage for logic
3	VO	(Variable)	Contrast Adjustment
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read L: Write
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bus line
8	DB1	H/L	Data bus line
9	DB2	H/L	Data bus line
10	DB3	H/L	Data bus line
11	DB4	H/L	Data bus line
12	DB5	H/L	Data bus line
13	DB6	H/L	Data bus line
14	DB7	H/L	Data bus line
15	A	—	Power supply for B/L(+)
16	K	—	Power supply for B/L(-)

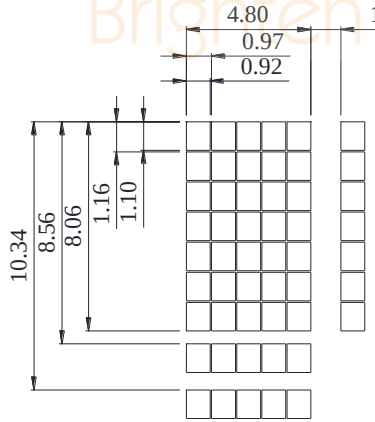
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Contour Drawing &Block Diagram





LED B/L

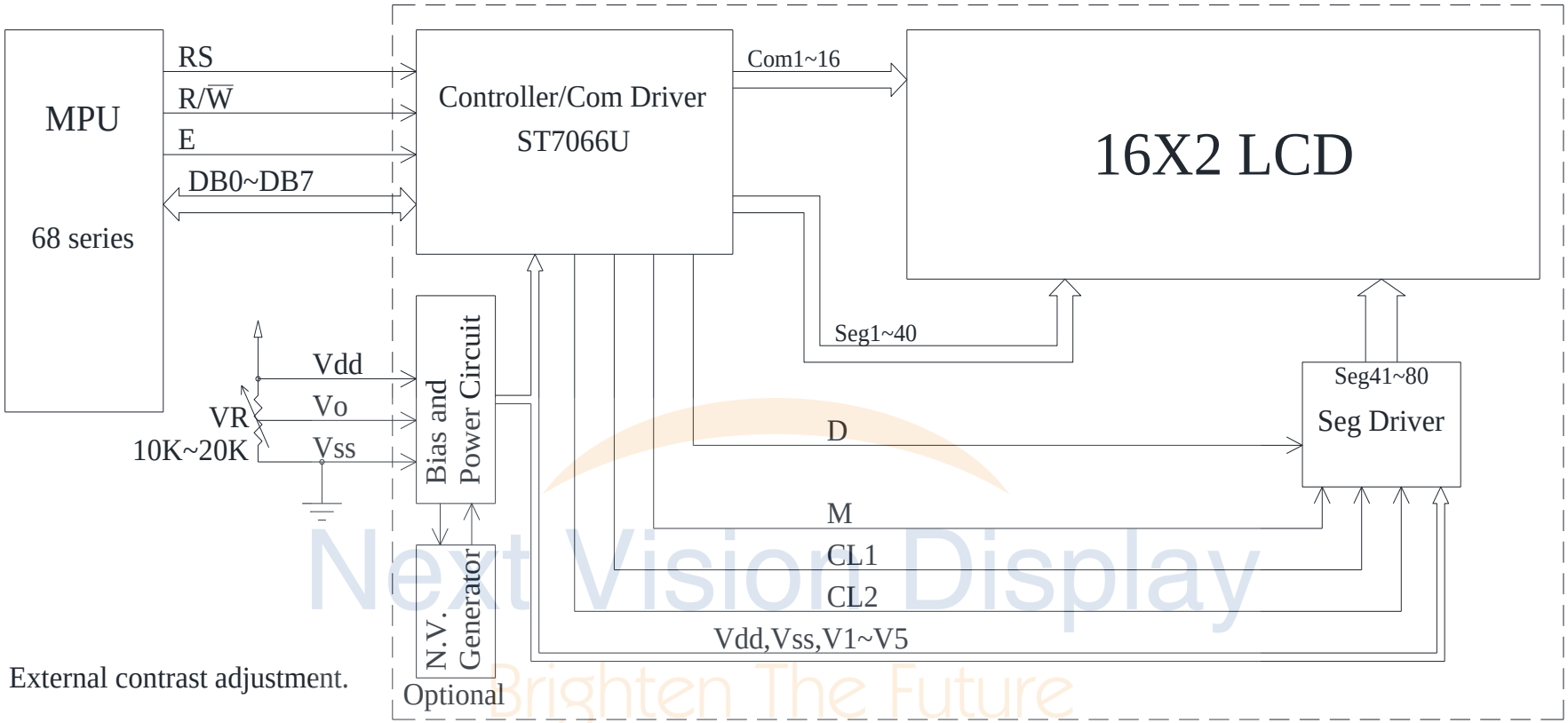


DOT SIZE
SCALE 4/1

PIN NO.	SYMBOL
1	Vss
2	Vdd
3	Vo
4	RS
5	R/$\overline{W}$
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7
15	A
16	K

The non-specified tolerance of dimension is ±0.3mm.

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Character located	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
DDRAM address	00	01	02	03	04	05	06	07	08	09	0A	0B	0C	0D	0E	0F
DDRAM address	40	41	42	43	44	45	46	47	48	49	4A	4B	4C	4D	4E	4F