Next Vision Display
Brighten The Future

NEXT VISION DISPLAY

SPECIFICATION

CUSTOMER : _____

MODULE NO.: NL015FN001ML

Version	A
Engineer	
Date	April 11, 2006

Next Vision Display
Brighten The Future

Customer:
Approved by

NEXT VISION DISPLAY CO., LTD.

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1. Basic Specifications

1.1 Display Specifications

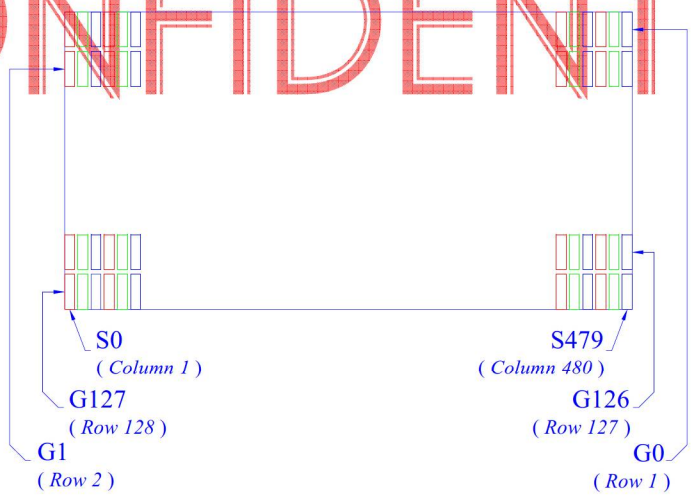
1) Display Mode: Passive Matrix
2) Display Color: 262,144 Colors (Maximum)
3) Drive Duty: 1/128 Duty

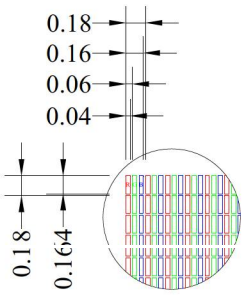
1.2 Mechanical Specifications

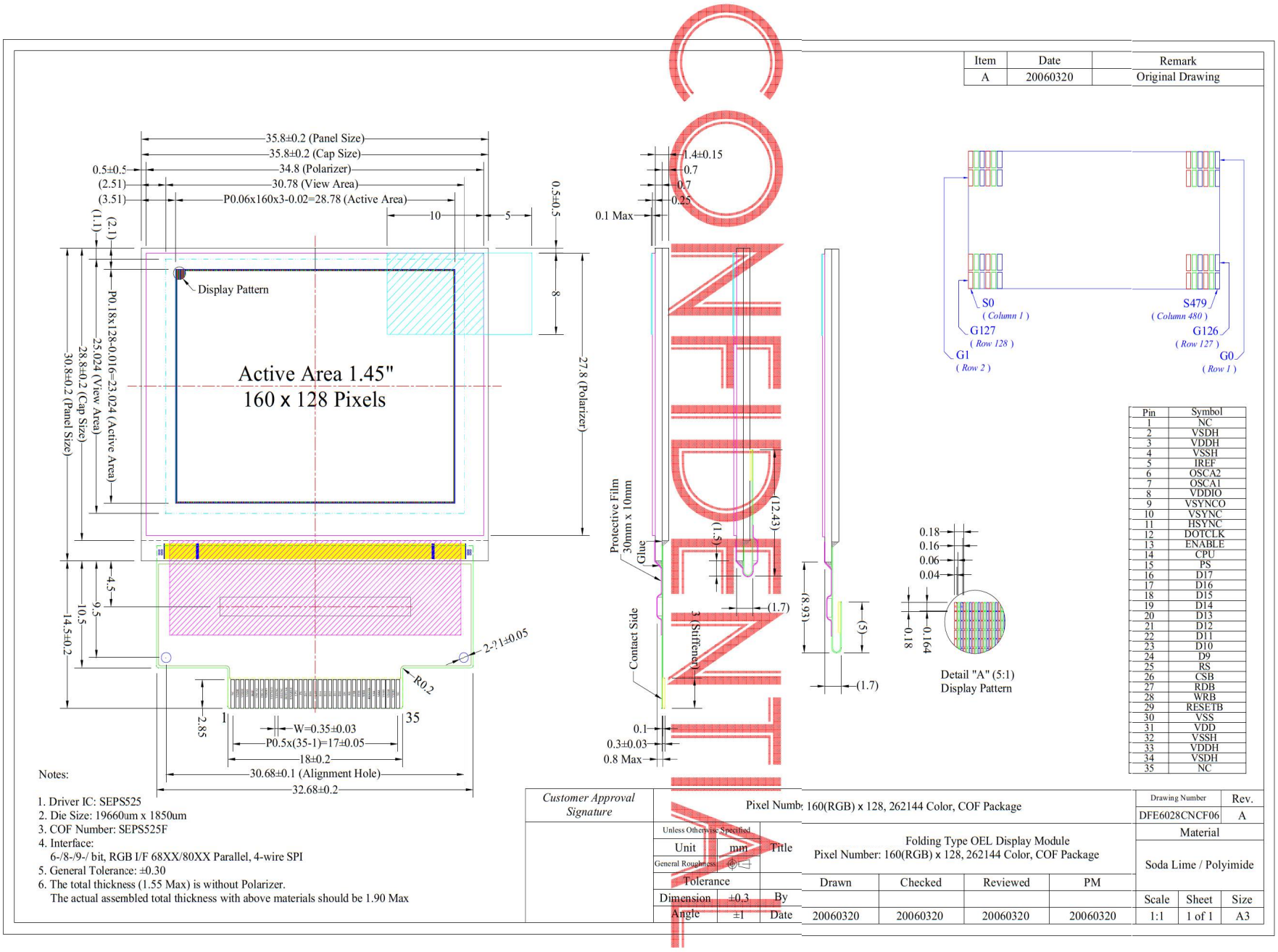
1) Outline Drawing: According to the annexed outline drawing number
2) Number of Pixels: 160 (RGB) × 128
3) Panel Size: 35.80 × 30.80 × 1.7 (mm)
4) Active Area: 28.78 × 23.024 (mm)
5) Pixel Pitch: 0.06 × 0.18 (mm)
6) Pixel Size: 0.04 × 0.164 (mm)
7) Weight: 3.6 (g)

1.3 Active Area & Pixel Construction

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Item	Date	Remark
A	20060320	Original Drawing

Pin	Symbol
1	NC
2	VSDH
3	VDDH
4	VSSH
5	IREF
6	OSCA2
7	OSCA1
8	VDDIO
9	VSYNCO
10	VSYNC
11	HSYNC
12	DOTCLK
13	ENABLE
14	CPU
15	PS
16	D17
17	D16
18	D15
19	D14
20	D13
21	D12
22	D11
23	D10
24	D9
25	RS
26	CSB
27	RDB
28	WRB
29	RESETB
30	VSS
31	VDD
32	VSSH
33	VDDH
34	VSDH
35	NC

Notes:

1. Driver IC: SEPS525
2. Die Size: 19660um x 1850um
3. COF Number: SEPS525F
4. Interface:
   6-/8-/9-/ bit, RGB I/F 68XX/80XX Parallel, 4-wire SPI
5. General Tolerance: ±0.30
6. The total thickness (1.55 Max) is without Polarizer.
   The actual assembled total thickness with above materials should be 1.90 Max

Customer Approval Signature	Pixel Numb: 160(RGB) x 128, 262144 Color, COF Package	Drawing Number	Rev.
		DFE6028CNCF06	A

Unless Otherwise Specified			Folding Type OEL Display Module Pixel Number: 160(RGB) x 128, 262144 Color, COF Package				Material		
Unit	mm	Title					Soda Lime / Polyimide		
General Roughness									
Tolerance			Drawn	Checked	Reviewed	PM	Scale	Sheet	Size
Dimension	±0.3	By							
Angle	±1	Date	20060320	20060320	20060320	20060320	1:1	1 of 1	A3

1.5 Pin Definition

<table>
<tr><th>Pin Number</th><th>Symbol</th><th>Type</th><th>Function</th></tr>
<tr><td colspan="4">Power Supply Pins</td></tr>
<tr><td>4,32</td><td>VSDH</td><td>P</td><td>Data Driver Ground</td></tr>
<tr><td>2,33</td><td>VSSH</td><td>P</td><td>Scan Driver Ground</td></tr>
<tr><td>3,34</td><td>VDDH</td><td>P</td><td>Data, Scan Driver Power Supply.</td></tr>
<tr><td>30</td><td>VSS</td><td>P</td><td>Power Supply Ground</td></tr>
<tr><td>31</td><td>VDD.</td><td>P</td><td>Logic Power Supply.</td></tr>
<tr><td>8</td><td>VDDIO</td><td>P</td><td>MPU I/F PAD Power Supply</td></tr>
<tr><td colspan="4">System Control Pins</td></tr>
<tr><td>5</td><td>IREF</td><td>I/O</td><td>Current Reference for Brightness Adjustment
Tie 70KΩ resistor to VSS.</td></tr>
<tr><td>6</td><td>OSCA2</td><td>O</td><td>Fine adjustment for oscillation
Tie 10 KΩ resistor to OSCA1 between OSCA2.
When the external clock mode is selected, OSCA1 is used external clock input.</td></tr>
<tr><td>7</td><td>OSCA1</td><td>I</td><td></td></tr>
<tr><td>14</td><td>CPU</td><td>I</td><td>Selects the CPU type
Low: 80-series CPU, High: 68-Series CPU.</td></tr>
<tr><td>15</td><td>PS</td><td>I</td><td>Selects parallel/Serial interface type
Low: serial, High: parallel.</td></tr>
<tr><td colspan="4">MPU Interface Pins</td></tr>
<tr><td>9</td><td>VSYNCO</td><td>O</td><td rowspan="5">RGB Mode Functional Pins
VSYNCO: Vertical Sync. Output
VSYNC: Vertical Sync. Input
HSYNC: Horizontal Sync. Input
DOTCLK: Dot Clock Input
ENABLE: Video Enable Input</td></tr>
<tr><td>10</td><td>VSYNC</td><td>I</td></tr>
<tr><td>11</td><td>HSYNC</td><td>I</td></tr>
<tr><td>12</td><td>DOTCLK</td><td>I</td></tr>
<tr><td>13</td><td>ENABLE</td><td>I</td></tr>
<tr><td>16~24</td><td>D17~D9</td><td>I/O</td><td>Host Data Input/Output Bus
These pins are 9-bit bi-directional data bus to be connected with MCU data bus.
<table>
<tr><th>PS</th><th>Description</th></tr>
<tr><td>1</td><td>8_bit bus : D[17:10]
9_bit bus : D[17:9]</td></tr>
<tr><td>0</td><td>D[17] SCL : Synchronous clock input
D[16] SDI : Serial data input
D[15] SDO : Serial data output</td></tr>
</table>
Fix unused pins to the VSS level.</td></tr>
<tr><td>25</td><td>RS</td><td>I</td><td>Selects the data/command
Low: command, High: parameter/data</td></tr>
<tr><td>26</td><td>CSB</td><td>I</td><td>Chip Select
Low: SEPS225 is selected and can be accessed.
High: SEPS225 is not selected and cannot be accessed.</td></tr>
<tr><td>27</td><td>RDB</td><td>I</td><td>Read or Read/Write Enable
80-system bus interface: read strobe signal (active low).
68-system bus interface: bus enable strobe (active high).
When serial mode, fix it to VDD or VSS level.</td></tr>
</table>

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1.5 Pin Definition (Continued)

Pin Number	Symbol	Type	Function
MPU Interface Pins (Continued)			
28	WRB	I	*Write or Read/Write Select* 80-system bus interface: write strobe signal (active low). 68-system bus interface: read/write select. Low: write, High: read. When serial mode, fix it to VDD or VSS level.
29	RESETB	I	*Chip Reset* Reset SEPS225 (active low)
Reserved Pins			
1,35	NC	-	*No Connection*

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