Specification for Approval

PROJECT PN: P27801

PRODUCT NUMBER: 9OL9927801000

PRODUCT DESCRIPTION: RGS11096096FR013

CUSTOMER
APPROVED BY
DATE:

first FC components

RITDISPLAY CORP. APPROVED
Represented by: first FC components **FIRST Components s.r.l.** Via Cavour, 69 21020 Buguggiate (VA) Italy Tel: +39 0332 1888005 Web: www.first.components.com

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1. SCOPE

The purpose of this specification is to define the general provisions and quality requirements that apply to the supply of display cells manufactured by RiTdisplay. This document, together with the Module Ass'y Drawing, is the highest-level specification for this product. It describes the product, identifies supporting documents and contains specifications.

2. WARRANTY

RiTdisplay warrants that the products delivered pursuant to this specification (or order) will conform to the agreed specifications for twelve (12) months from the shipping date ("Warranty Period"). RiTdisplay is obligated to repair or replace the products which are found to be defective or inconsistent with the specifications during the Warranty Period without charge, on condition that the products are stored in the original packages at 25°C±5°C, 55%±10%RH or used as the conditions specified in the specifications.
Nevertheless, RiTdisplay is not obligated to repair or replace the products without charge if the defects or inconsistency are caused by the force majeure or the reckless behaviors of the customer.
After the Warranty Period, all repairs or replacements of the products are subject to charge.

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3. FEATURES

- Small molecular organic light emitting diode.
- Color : 262K color and 65K colors
- Panel matrix : 96x96
- Driver IC : SSD1351
- Excellent quick response time.
- Extremely thin thickness for best mechanism design : 1.227mm
- High contrast : 10,000:1
- Wide viewing angle : 160°
- 8/16-bit 8080-series Parallel Interface, Serial Peripheral Interface.
- Wide range of operating temperature : -40 to 70 °C
- Anti-glare polarizer.

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4. MECHANICAL DATA

NO	ITEM	SPECIFICATION	UNIT
1	Dot Matrix	96 (W) x (RxGxB) x 96 (H)	dot
2	Dot Size	0.045 (W) x 0.19 (H)	mm^2
3	Dot Pitch	0.07 (W) x 0.21 (H)	mm^2
4	Aperture Rate	58	%
5	Active Area	20.135 (W) x 20.14 (H)	mm^2
6	Panel Size	25.8 (W) x 30.1 (H)	mm^2
7	Panel Thickness	1.02 ± 0.1	mm
8	Module Size	25.8 (W) x 48.1 (H) x 1.227 (D)	mm^3
9	Diagonal A/A size	1.12	inch
10	Module Weight	1.89 ± 10%	gram

* Panel thickness includes substrate glass, cover glass and UV glue thickness.

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8.3 PIN ASSIGNMENTS

PIN NO	PIN NAME	DESCRIPTION
1	NC	Not Connected.
2	VLSS	Analog system ground pin.
3	VCC	Power supply for panel driving voltage.
4	VCI	Low voltage power supply VCI must always be equal to or higher than VDD and VDDIO.
5	VDD	Power supply pin for core logic operation.
6	IREF	A resistor should be connected between this pin and VSS.
7	RES#	This pin is reset signal input.
8	D/C#	This pin is Data/Command control pin connecting to the MCU.
9	CS#	This pin is the chip select input connecting to the MCU.
10	BS1	MCU bus interface selection pins.
11	BS0	
12	R/W#	This pin is read / write control input pin connecting to the MCU interface.
13	E/RD#	This pin is MCU interface input. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial interface is selected, this pin E(RD#) must be connected to VSS.
14	D0	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. (Except for D2 pin in SPI mode)
15	D1	
16	D2	
17	D3	
18	D4	
19	D5	
20	D6	
21	D7	
22	D8	
23	D9	
24	D10	
25	D11	
26	D12	
27	D13	

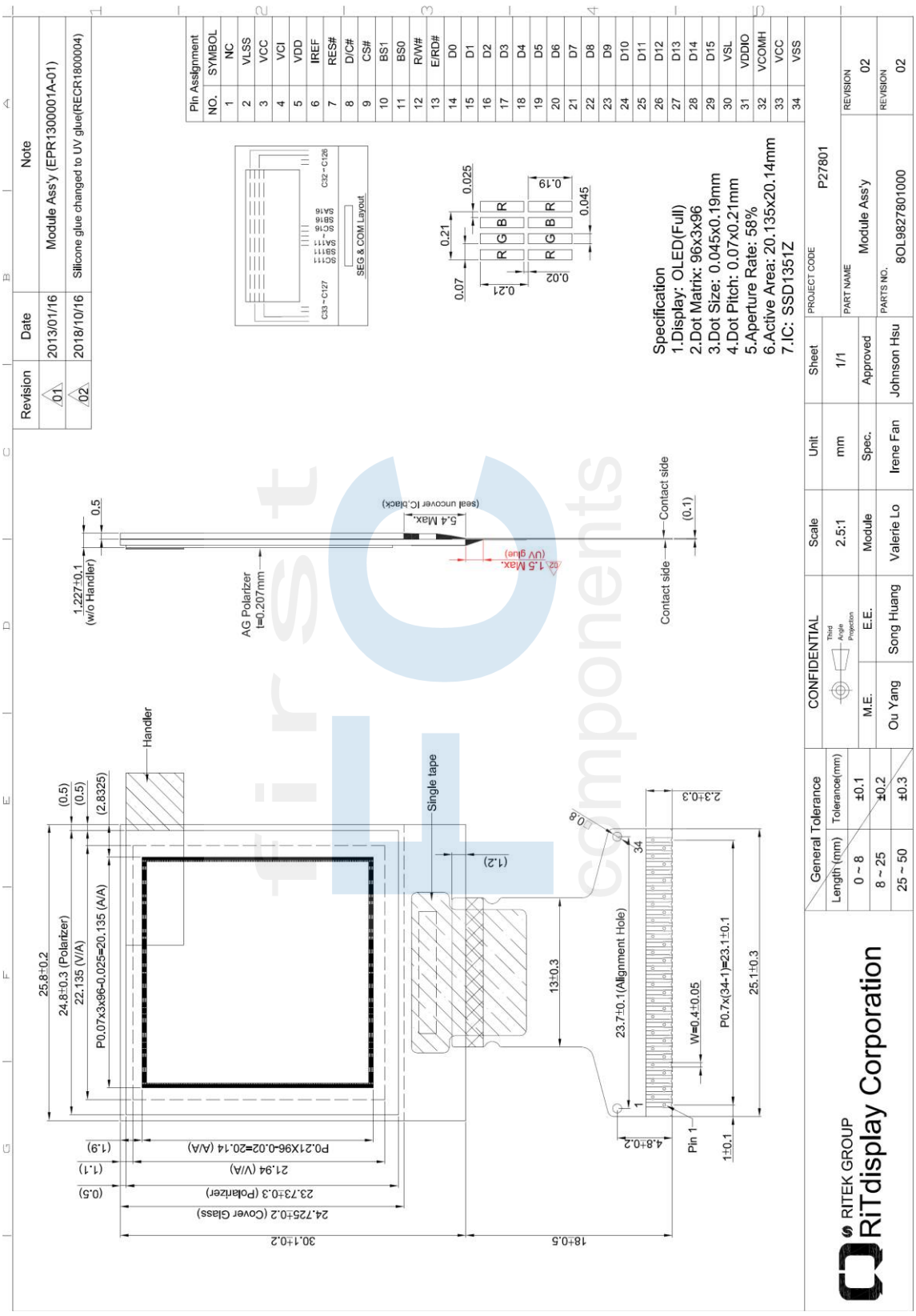
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28	D14	
29	D15	
30	VSL	This is segment voltage reference pin. External VSL is set as default. This pin has to connect with resistor and diode to ground. (Details depend on application)
31	VDDIO	Power supply for interface logic level.
32	VCOMH	A capacitor should be connected between this pin and VSS.
33	VCC	Power supply for panel driving voltage.
34	VSS	Ground pin

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11. EXTERNAL DIMENSION



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