Specification for Approval

PROJECT PN: **P43501**

PRODUCT NUMBER: **9OL9943501000**

PRODUCT DESCRIPTION: **RGS10064128FR005**

CUSTOMER
APPROVED BY
DATE:

first FC components

RITDISPLAY CORP. APPROVED
Represented by: first FC components **FIRST Components s.r.l.** Via Cavour, 69 21020 Buguggiate (VA) Italy Tel: +39 0332 1888005 Web: www.first.components.com

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1. SCOPE

The purpose of this specification is to define the general provisions and quality requirements that apply to the supply of display cells manufactured by RiTdisplay. This document, together with the Module Assembly Drawing, is the highest-level specification for this product. It describes the product, identifies supporting documents and contains specifications.

2. WARRANTY

RiTdisplay warrants that the products delivered pursuant to this specification (or order) will conform to the agreed specifications for twelve (12) months from the shipping date ("Warranty Period"). RiTdisplay is obligated to repair or replace the products which are found to be defective or inconsistent with the specifications during the Warranty Period without charge, on condition that the products are stored in the original packages at 25℃±5℃, 55%±10%RH or used as the conditions specified in the specifications.
Nevertheless, RiTdisplay is not obligated to repair or replace the products without charge if the defects or inconsistency are caused by the force majeure or the reckless behaviors of the customer.
After the Warranty Period, all repairs or replacements of the products are subject to charge.

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3. FEATURES

- Small molecular organic light emitting diode.
- Color : Full
- Panel resolution : 64x3x128
- Driver IC : SSD1357Z
- Excellent quick response time.
- Extremely thin thickness for best mechanism design : 1.02 mm
- High contrast : 10,000:1
- Wide viewing angle : 160°
- Interface: 8080-series parallel Interface and Serial Peripheral Interface
- Wide range of operating temperature : -40 to 70℃
- Anti-glare polarizer.
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4. MECHANICAL DATA

NO	ITEM	SPECIFICATION	UNIT
1	Dot Matrix	64 x 3 x 128	dot
2	Dot Size	0.037 (W) x 0.15 (H)	mm^2
3	Dot Pitch	0.057 (W) x 0.17 (H)	mm^2
4	Active Area	10.924 (W) x 21.74 (H)	mm^2
5	Panel Size	14.86 (W) x 28.4 (H)	mm^2
6*	Panel Thickness	1.02 ± 0.1	mm
7	Module Size	14.86 (W) x 43.7 (H) x 1.02 (T)	mm^3
8	Diagonal A/A size	0.96	inch
9	Module Weight	1.05 ± 10%	gram

* Panel thickness includes substrate glass, cover glass and UV glue thickness.

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8.3 PIN ASSIGNMENTS

Pin No.	Pin Name	Description	Setting at each interface		
			8080 Parallel	SPI	IIC
1	VLSS	Analog system ground pin. It must be connected to external ground.			
2	VSL	This is segment voltage reference pin.			
3	VCOMH	COM signal deselected voltage level.			
4	VP	This pin is the segment pre-charge voltage reference pin.			
5	VCC	Power supply for panel driving voltage.			
6	IREF	This pin is the segment output current reference pin.			
7	VSS	Ground pin.			
8	VDD	A capacitor should be connected between this pin and VSS.			
9	RES#	This pin is reset signal input.	RES#	RES#	NC
10	D/C#	This pin is Data/Command control pin connecting to the MCU.	D/C#	D/C#	NC
11	CS#	This pin is the chip select input connecting to the MCU.	CS#	CS#	NC
12	BS2	MCU bus interface selection pin	High	Low	NC
13	BS1		High	Low	NC
14	BS0		Low	Low	NC
15	R/W#	This pin is read / write control input pin connecting to the MCU interface.	R/W#	Low	NC
16	RD#	This pin is MCU interface input.	RD#	Low	NC
17	VDD	A capacitor should be connected between this pin and VSS.			
18	D0	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will	D0	SCLK	NC
19	D1		D1	SDIN	NC
20	D2		D2	Low	NC

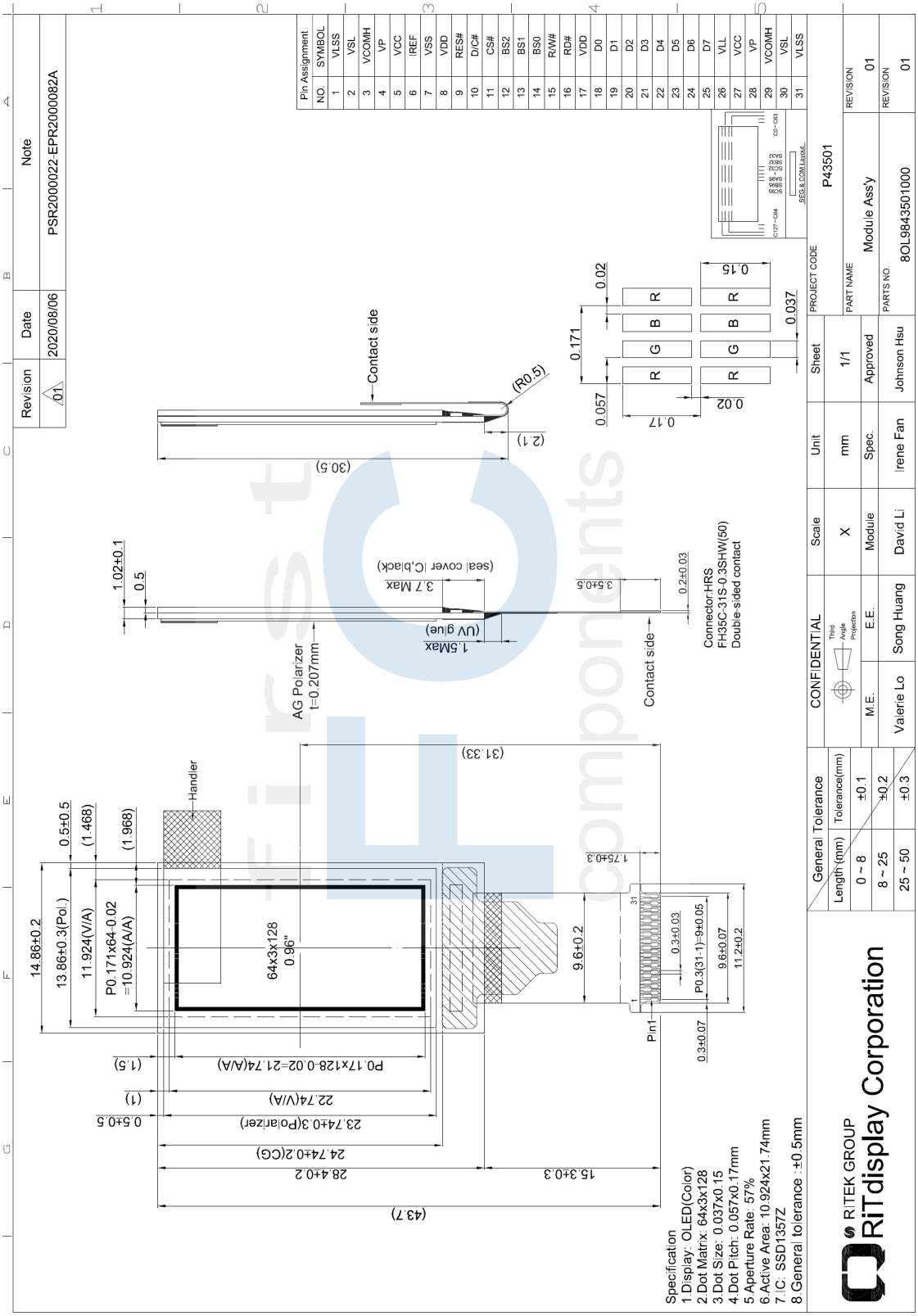
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21	D3	be the serial data input: SDIN.	D3	Low	NC
22	D4		D4	Low	NC
23	D5		D5	Low	NC
24	D6		D6	Low	NC
25	D7		D7	Low	NC
26	VLL	Ground pin.			
27	VCC	Power supply for panel driving voltage.			
28	VP	This pin is the segment pre-charge voltage reference pin.			
29	VCOMH	COM signal deselected voltage level.			
30	VSL	This is segment voltage (output low level) reference pin.			
31	VLSS	Analog system ground pin. It must be connected to external ground.			

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11. EXTERNAL DIMENSION



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