Preliminary Specification

PROJECT P/N: **P43601**
PRODUCT NUMBER: **9OL9943601000**
PRODUCT DESCRIPTION: **RGS10096064FR023**

CUSTOMER
APPROVED BY
DATE:

first FC components

RITDISPLAY CORP. APPROVED

Represented by:

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1. SCOPE

The purpose of this specification is to define the general provisions and quality requirements that apply to the supply of display cells manufactured by RiTdisplay. This document, together with the Module Assembly Drawing, is the highest-level specification for this product. It describes the product, identifies supporting documents and contains specifications.

2. WARRANTY

RiTdisplay warrants that the products delivered pursuant to this specification (or order) will conform to the agreed specifications for twelve (12) months from the shipping date ("Warranty Period"). RiTdisplay is obligated to repair or replace the products which are found to be defective or inconsistent with the specifications during the Warranty Period without charge, on condition that the products are stored in the original packages at 25°C±5°C, 55%±10%RH or used as the conditions specified in the specifications.
Nevertheless, RiTdisplay is not obligated to repair or replace the products without charge if the defects or inconsistency are caused by the force majeure or the reckless behaviors of the customer.
After the Warranty Period, all repairs or replacements of the products are subject to charge.

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3. FEATURES

- Small molecular organic light emitting diode.
- Color : Full
- Panel resolution : 96x3x64
- Driver IC : SSD1357Z
- Excellent quick response time.
- Extremely thin thickness for best mechanism design : 1.227 mm
- High contrast : 10,000:1
- Wide viewing angle : 160°
- Interface: 8-bit 6800/8080-series parallel interface,4-wire serial peripheral interface,I^2C Interface
- Wide range of operating temperature : -40 to 70°C
- Anti-glare polarizer.

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4. MECHANICAL DATA

NO	ITEM	SPECIFICATION	UNIT
1	Dot Matrix	96 x 3 x 64	dot
2	Dot Size	0.04 (W) x 0.19 (H)	mm^2
3	Dot Pitch	0.07 (W) x 0.21 (H)	mm^2
4	Active Area	20.13 (W) x 13.42 (H)	mm^2
5	Panel Size	24 (W) x 20.5 (H)	mm^2
6*	Panel Thickness	1.02 ± 0.15	mm
7	Module Size	24 (W) x 32.58 (H) x 1.227 (T)	mm^3
8	Diagonal A/A size	0.95	inch
9	Module Weight	TBD	gram

* Panel thickness includes substrate glass, cover glass and UV glue thickness.

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8.3 PIN ASSIGNMENTS

PIN NAME	PIN NO.	DESCRIPTION	Setting at each interface		
			16-8080 parallel	SPI	IIC
1	VLSS	Analog system ground pin. It must be connected to external ground.			
2	VSL	This is segment voltage reference pin.			
3	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS.			
4	VP	This pin is the segment pre-charge voltage reference pin.			
5	VCC	Power supply for panel driving voltage.			
6	IREF	This is an internal voltage reference pin. A capacitor should be connected to this pin and VSS.			
7	VSS	Ground pin.			
8	RES#	This is a reset signal input. Low active.	RES#	RES#	RES#
9	D/C#	This pin is Data/Command control pin.	D/C#	D/C#	SA0
10	CS#	This pin is the chip select input. Low active.	CS#	CS#	Low
11	BS2	MCU bus interface selection pins.	High	Low	Low
12	BS1		High	Low	High
13	R/W#	This pin is read / write control input pin connecting to the MCU interface.	WR#	Low	Low
14	RD#	This pin is MCU interface input.	RD#	Low	Low
15	VDD	Power supply pin for core logic operation. A capacitor should be connected between this pin and VSS.			
16	D0	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie Low. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN. When I^2C mode is selected, D2, D1 should be tied together and serve as SDAout,SDAin in application and D0 is the serial clock input, SCL.	D0	SCLK	SCL
17	D1		D1	SDIN	SDA_{IN}
18	D2		D2	Low	SDA_{OUT}
19	D3		D3	Low	Low
20	D4		D4	Low	Low
21	D5		D5	Low	Low
22	D6		D6	Low	Low
23	D7		D7	Low	Low

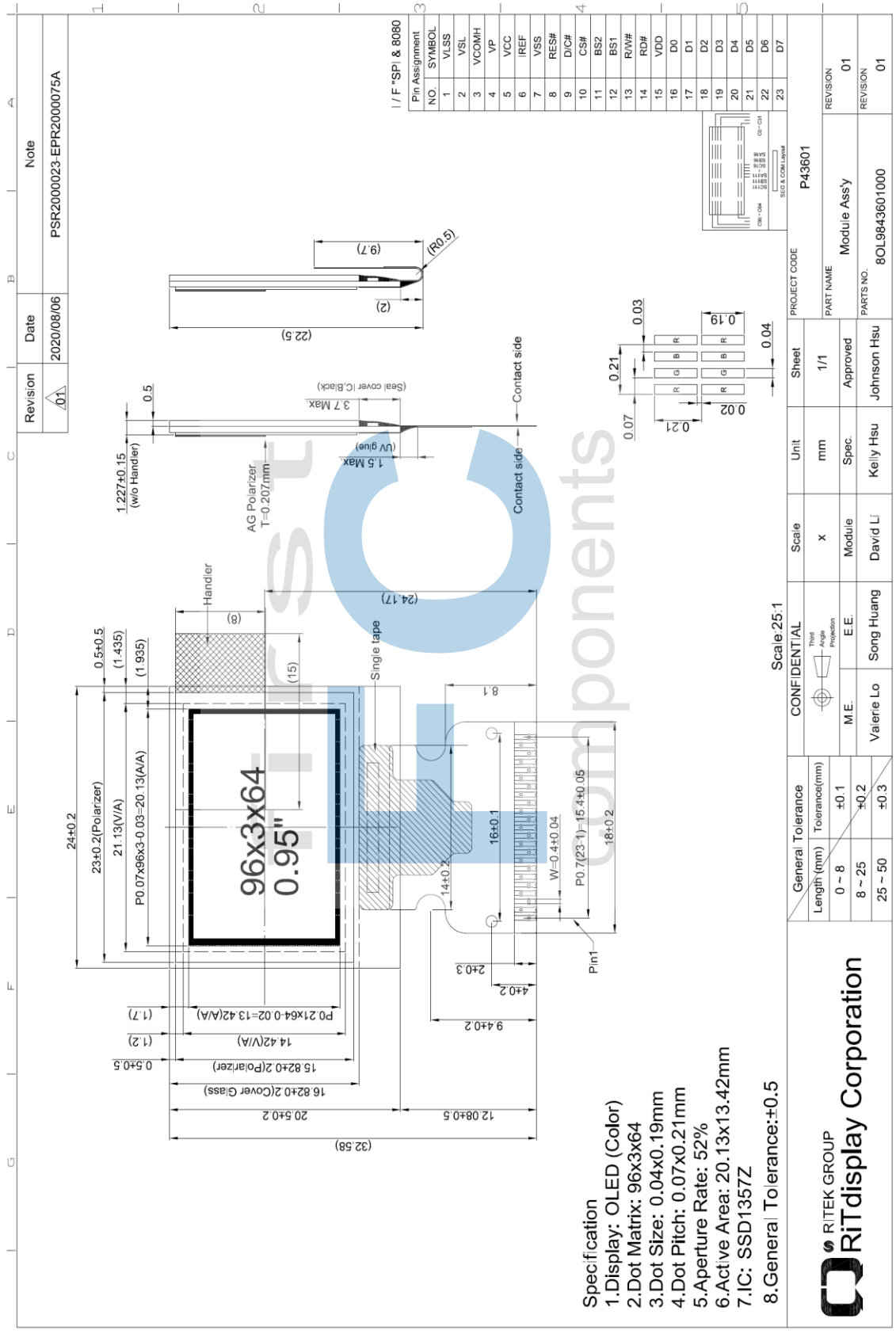
Note

(1) Low is connected to VSS

(2) High is connected to VDD

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11. EXTERNAL DIMENSION



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