SPECIFICATION Version: F

CUSTOMER :	
MODULE NO. :	WEO12864W#Q02D-FC

APPROVED BY: (FOR CUSTOMER USE ONLY)	

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
RELEASE DATE:			

■APPROVAL FOR SPECIFICATIONS ONLY

□APPROVAL FOR SPECIFICATIONS AND SAMPLE

Represented by:

FIRST Components s.r.l.
www.first-components.com
technical@first-components.com
+39 0332 1888005

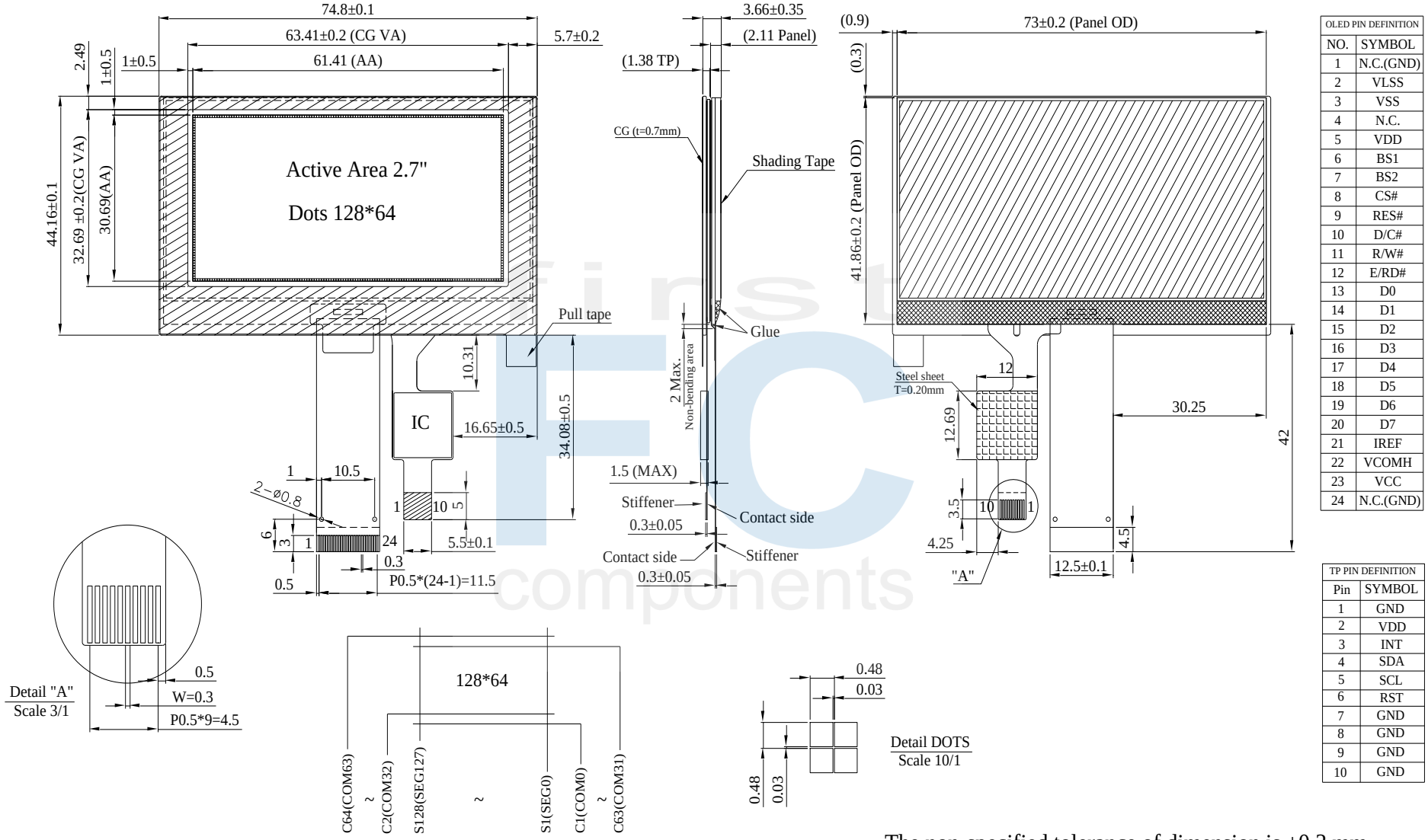
2.General Specification

Item	Dimension	Unit
Dot Matrix	128 x 64	—
Module dimension	74.8 × 44.16 × 3.66	mm
Active Area	61.41 × 30.69	mm
Pixel Size	0.45 × 0.45	mm
Pixel Pitch	0.48 × 0.48	mm
Display Mode	Passive Matrix	
Display Color	Monochrome (White)	
Drive Duty	1/64 Duty	
OLED IC	SSD1309	
OLED Interface	6800,8080,4-Wire SPI,I2C	
Size	2.7 inch	

CTP IC	GT911
Detect Point	1
CTP Interface	I2C
Surface	Normal Glare

first components

3. Contour Drawing & Block Diagram



OLED PIN DEFINITION	
NO.	SYMBOL
1	N.C.(GND)
2	VLSS
3	VSS
4	N.C.
5	VDD
6	BS1
7	BS2
8	CS#
9	RES#
10	D/C#
11	R/W#
12	E/RD#
13	D0
14	D1
15	D2
16	D3
17	D4
18	D5
19	D6
20	D7
21	IREF
22	VCOMH
23	VCC
24	N.C.(GND)

TP PIN DEFINITION	
Pin	SYMBOL
1	GND
2	VDD
3	INT
4	SDA
5	SCL
6	RST
7	GND
8	GND
9	GND
10	GND

The non-specified tolerance of dimension is ±0.3 mm .

4. Interface Pin Function

<table>
<tr><th>No.</th><th>Symbol</th><th>Function</th></tr>
<tr><td>1</td><td>NC(GND)</td><td>No connection</td></tr>
<tr><td>2</td><td>VLSS</td><td>This is an analog ground pin</td></tr>
<tr><td>3</td><td>VSS</td><td>Ground.</td></tr>
<tr><td>4</td><td>NC</td><td>No connection</td></tr>
<tr><td>5</td><td>VDD</td><td>Power supply pin for core logic operation</td></tr>
<tr><td>6</td><td>BS1</td><td rowspan="2">MCU bus interface selection pins. Select appropriate logic setting as described in the following table. BS2 and BS1 are pin select
<table>
<tr><th></th><th>BS1</th><th>BS2</th></tr>
<tr><td>I2C</td><td>1</td><td>0</td></tr>
<tr><td>4-wire Serial</td><td>0</td><td>0</td></tr>
<tr><td>8-bit 68XX Parallel</td><td>0</td><td>1</td></tr>
<tr><td>8-bit 80XX Parallel</td><td>1</td><td>1</td></tr>
</table>
Note
(1) 0 is connected to VSS
(2) 1 is connected to VDD</td></tr>
<tr><td>7</td><td>BS2</td></tr>
<tr><td>8</td><td>CS#</td><td>This pin is the chip select input connecting to the MCU.
The chip is enabled for MCU communication only when CS# is pulled LOW (active LOW).</td></tr>
<tr><td>9</td><td>RES#</td><td>This pin is reset signal input.
When the pin is pulled LOW, initialization of the chip is executed.
Keep this pin pull HIGH during normal operation.</td></tr>
<tr><td>10</td><td>D/C#</td><td>This pin is Data/Command control pin connecting to the MCU.
When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data.
When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register.
In I2C mode, this pin acts as SA0 for slave address selection.</td></tr>
<tr><td>11</td><td>R/W#</td><td>This pin is read / write control input pin connecting to the MCU interface.
When 6800 interface mode is selected, this pin will be used as Read/Write (R/W#) selection input. Read mode will be carried out when this pin is pulled HIGH and write mode when LOW.
When 8080 interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected.
When serial or I2C interface is selected, this pin must be connected to VSS.</td></tr>
</table>

12	E/RD#	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD#) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
13-20	D0~D7	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SDIN and D2 should be kept NC. When I2C mode is selected, D2, D1 should be tied together and serve as SDAout, SDAin in application and D0 is the serial clock input, SCL.
21	IREF	This pin is the segment output current reference pin. IREF is supplied externally.
22	VCOMH	COM signal deselected voltage level. A capacitor should be connected between this pin and VSS.
23	VCC	Power supply for panel driving voltage. This is also the most positive power voltage supply pin.
24	NC(GND)	No connection

CTP PIN Definition

No.	Symbol	Function
1	GND	Power ground
2	VDD	Power supply
3	INT	Interrupt signal, active low, asserted to request Host start a new transaction
4	SDA	I2C data signal
5	SCL	I2C clock signal
6	RST	External reset signal, active low
7	GND	Power ground
8	GND	Power ground
9	GND	Power ground
10	GND	Power ground

5.Absolute Maximum Ratings

5.1 Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit	Notes
Supply Voltage for Logic	VDD	-0.3	4	V	1, 2
Supply Voltage for Display	VCC	0	15	V	1, 2
Operating Temperature	TOP	-20	+70	°C	-
Storage Temperature	TSTG	-30	+80	°C	-

Note 1: All the above voltages are on the basis of “VSS = 0V”.
Note 2: When this module is used beyond the above absolute maximum ratings, permanent breakage of the module may occur. Also, for normal operations, it is desirable to use this module under the conditions according to Section 6 ‘’Electrical Characteristics”. If this module is used beyond these conditions, malfunctioning of the module can occur and the reliability of the module may deteriorate

5.2 Touch Panel Controller GT911

Parameter	Symbol	Min	Max	Unit	Notes
Power Supply Voltage	VDD	2.66	3.47	V	1

Note 1: If used beyond the absolute maximum ratings, CTP IC may be permanently damaged. It is strongly recommended that the device be used within the electrical characteristics in normal operations. If exposed to the condition not within the electrical characteristics, it may affect the reliability of the device.