SPECIFICATION Version: 0

CUSTOMER :

MODULE NO. : WEG10016L#DP3N00100-FC

APPROVED BY: (FOR CUSTOMER USE ONLY)	

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
RELEASE DATE:			

■APPROVAL FOR SPECIFICATIONS ONLY

□APPROVAL FOR SPECIFICATIONS AND SAMPLE

Represented by:

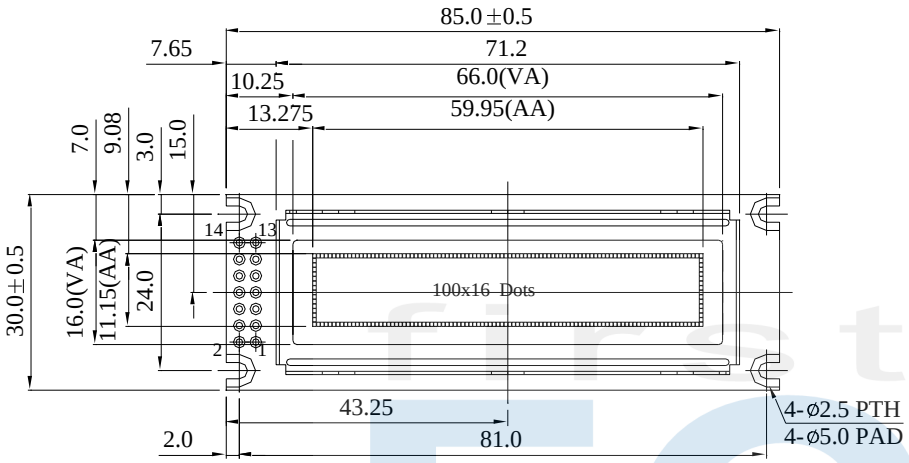
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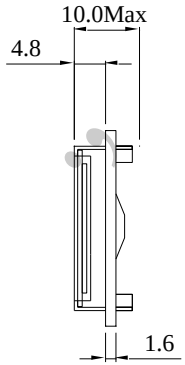
2.General Specification

Item	Dimension	Unit
Dot Matrix	100*16 Dots	—
Module dimension	85.0 x 30.0 x 10.0(MAX)	mm
View area	66.0 x 16.0	mm
Active area	59.95 x 11.15	mm
Dot size	0.55 x 0.65	mm
Dot pitch	0.60 x 0.70	mm
Panel Type	OLED , Yellow	
Duty	1/16	
IC	WS0010-TX	
Interface	6800	
Size	2.4 inch	

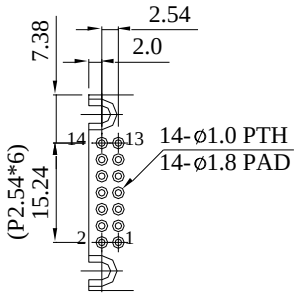
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3. Contour Drawing & Block Diagram

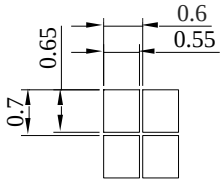




PIN NO.	SYMBOL
1	VDD
2	VSS
3	NC
4	RS
5	R/$\overline{W}$
6	E
7	DB0
8	DB1
9	DB2
10	DB3
11	DB4
12	DB5
13	DB6
14	DB7



PIN DETAIL



DOT SIZES
SCALE 10/1

The non-specified tolerance of dimension is ±0.3 mm .

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4.Interface Pin Function

Pin No.	Symbol	Level	Description
1	VDD	3.3V	Supply Voltage for logic
2	VSS	0V	Ground
3	NC	—	
4	RS	H/L	H: DATA, L: Instruction code
5	R/W	H/L	H: Read(Module→MPU) L: Write(MPU→Module)
6	E	H,H→L	Chip enable signal
7	DB0	H/L	Data bit 0
8	DB1	H/L	Data bit 1
9	DB2	H/L	Data bit 2
10	DB3	H/L	Data bit 3
11	DB4	H/L	Data bit 4
12	DB5	H/L	Data bit 5
13	DB6	H/L	Data bit 6
14	DB7	H/L	Data bit 7

5.Absolute Maximum Ratings

Item	Symbol	Min	Max	Unit	Notes
Operating Temperature	TOP	-40	+80	℃	
Storage Temperature	TST	-40	+85	℃	
Supply Voltage For Logic	VDD-VSS	-0.3	3.6	V	

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