Winstar Display Co., LTD
華凌光電股份有限公司
WEB: https://www.winstar.com.tw E-mail: sales@winstar.com.tw

SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF50-800480FS#ASP7-FC

APPROVED BY: **(FOR CUSTOMER USE ONLY)**	**PCB VERSION:** **DATA:**

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
first FC components			葉虹蘭
ISSUED DATE: 2022/06/10			

General Specifications

Item	Dimension	Unit
Size	5.0	inch
Dot Matrix	800× 3(RGB) × 480	dots
Module dimension	138.7 (W) x 75.8 (H) x 13.0(D)(MAX)	mm
Active area	108(W) ×64.8 (H)	mm
Pixel pitch	0.135(W) ×0.135(H)	mm
LCD type	TFT, Normally Black, Transmissive	
View Direction	80/80/80/80	
Aspect Ratio	5:3	
Backlight Type	LED,Normally White	
TFT Controller IC	BT815Q	
TFT Interface	SPI/QSPI	
CTP IC	ILI2130 or Equivalent	
CTP Interface	I2C	
CTP FW Version	0x07.0x00.0x00.0x00.0xA1.0x25.0x50.0x00	
CTP Resolution	16384*16384	
With /Without TP	With CTP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage

first components

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

first FC components

Electrical Characteristics

1. Operating conditions:

Item	Symbol	Condition	Min	Typ	Max	Unit	Remark
Supply Voltage For VDD	VDD	—	3.1	3.3	3.5	V	—
Supply Current For VDD	IDD	VDD=3.3V	—	147	220	mA	Note1,2
Supply Voltage For VLED	VLED	—	4.5	5	5.5	V	—
Supply Current For VLED	ILED	VLED=+5V	—	334	500	mA	Note1

Note 1 : This(Typ) value is test for VDD=3.3V ,VLED=+5V , Ta=25 ℃ only

Note 2 : VDD Power consumption is include CTP driver system

2. Backlight PWM conditions

Parameter	Min.	Typ.	Max.	Unit	Remark
PWM Control Duty Ratio	0	-	100	%	Note 4,5,6
PWM Control Frequency	250	-	10K	Hz	
LED Life Time	-	50,000	-	Hr	Note 1,2,3

Note 1 : Ta = 25 ℃

Note 2 : Brightness to be decreased to 50% of the initial value

Note 3 : The single LED lamp case

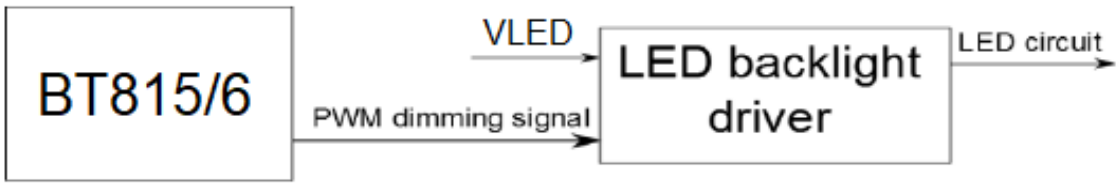
Note 4 : Turn ON the backlight when PWM =Low, Turn OFF when PWM =High

Note 5 : PWM Low Active Control from lowest brightness to highest brightness

Note 6 : Backlight PWM signal is internally connected to BT815/6 backlight control pin, This pin is controlled by BT815/6 registers

Note 7: The backlight dimming control pin (BACKLIGHT) is a pulse width modulated (PWM) signal controlled by two registers: REG_PWM_HZ and REG_PWM_DUTY. REG_PWM_HZ specifies the PWM output frequency, the range is 250-10000 Hz. REG_PWM_DUTY specifies the duty cycle; the range is 0-128. A value of 0 means that the PWM is completely off and 128 means completely on.

The BACKLIGHT pin will output low when the DISP pin is not enabled (i.e. logic 0).



Optical Characteristics

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr+Tf	θ=0°、Φ=0°	-	30	40	.ms	Note 3
Contrast ratio		CR	At optimized viewing angle	800	1000	-	-	Note 4
Color Chromaticity	White	Wx	θ=0°、Φ=0	0.27	0.32	0.37		Note 2,6,7
		Wy		0.295	0.345	0.395		
Viewing angle	Hor.	ΘR	CR≧10	70	80	-	Deg.	Note 1
		ΘL		70	80	-		
	Ver.	ΦT		70	80	-		
		ΦB		70	80	-		
Brightness		-	-	650	750	-	cd/m^2	Center of display
Uniformity		(U)	-	75	-	-	%	Note5

Ta=25±2℃

Note 1: Definition of viewing angle range

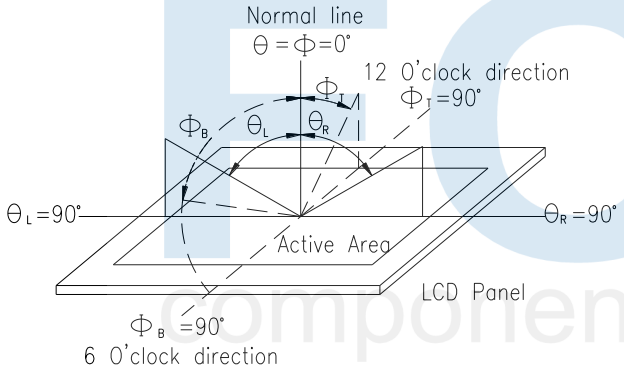


Fig. 8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7or BM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

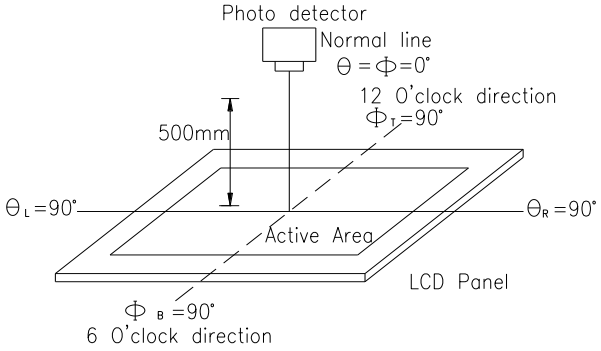


Fig. 8.2. Optical measurement system setup

Note 3: Definition of Response time:

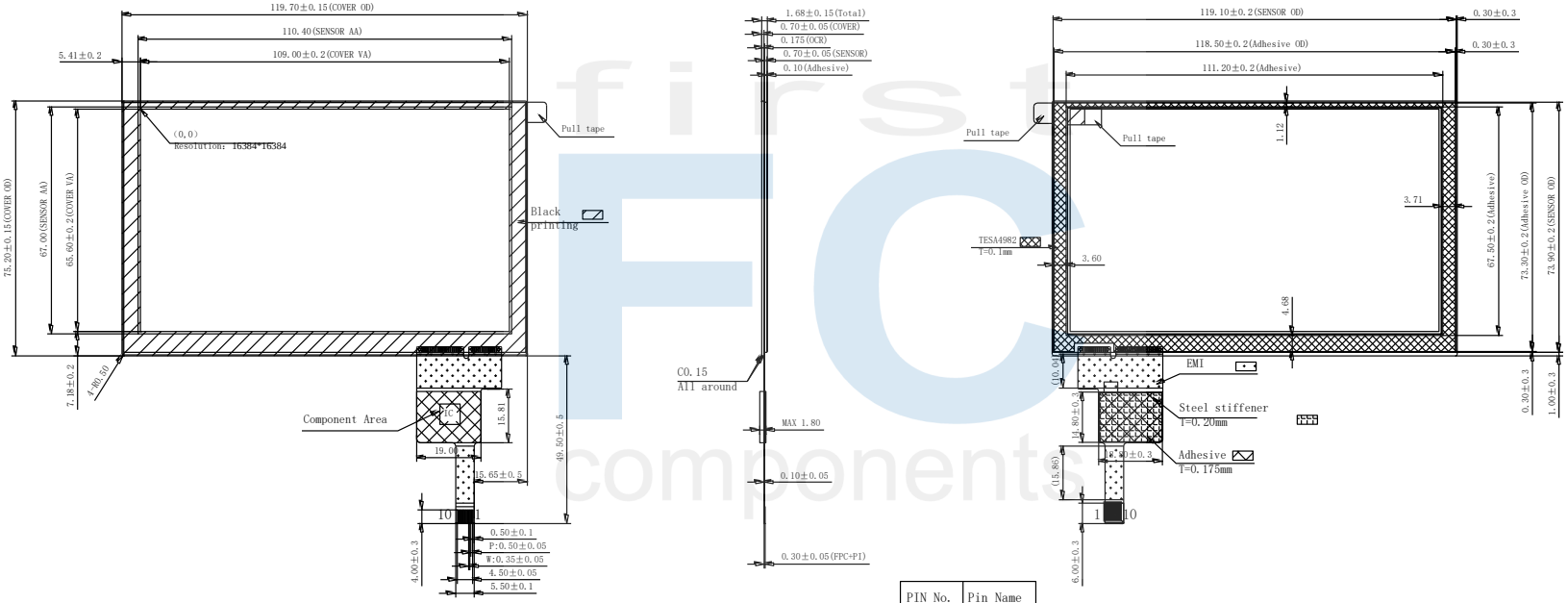
The response time is defined as the LCD optical switching time interval between “White” state

Interface

LCM PIN Definition (CON 5/CON 8)

Pin	Symbol	I/O	Function
1	VDD	P	3.3V power supply input
2	GND	P	Ground
3	SCK	I	SPI clock input
4	MISO	O	SPI Single mode: SPI MISO output SPI Dual/Quad mode: SPI data line 1
5	MOSI	I	SPI Single mode: SPI MOSI input SPI Dual/Quad mode: SPI data line 0
6	CS	I	SPI slave select input
7	INT	OD/O	Interrupt to host, open drain output(default) or push-pull output, active low
8	PD_N	I	Chip power down mode control input, active low. Connect to MCU GPIO for power management or hardware reset function
9	GPIO2	I/O	General purpose IO 2
10	GPIO3	I/O	General purpose IO 3
11	IO2	I/O	SPI Single/Dual mode: General purpose IO 0 SPI Quad mode: SPI data line 2
12	IO3	I/O	SPI Single/Dual mode: General purpose IO 1 SPI Quad mode: SPI data line 3
13	CSCL	I	Connect to I2C SCL pin of the CTP(ILI2130)
14	CSDA	I/O	Connect to I2C SDA pin of the CTP(ILI2130)
15	CRST	I	Connect to reset pin of the CTP(ILI2130)
16	CINT	O	Connect to interrupt pin of the CTP(ILI2130)
17	VLED	P	5V power supply input
18	VLED	P	5V power supply input
19	GND	P	Ground
20	GND	P	Ground

Touch Panel Information



Front view

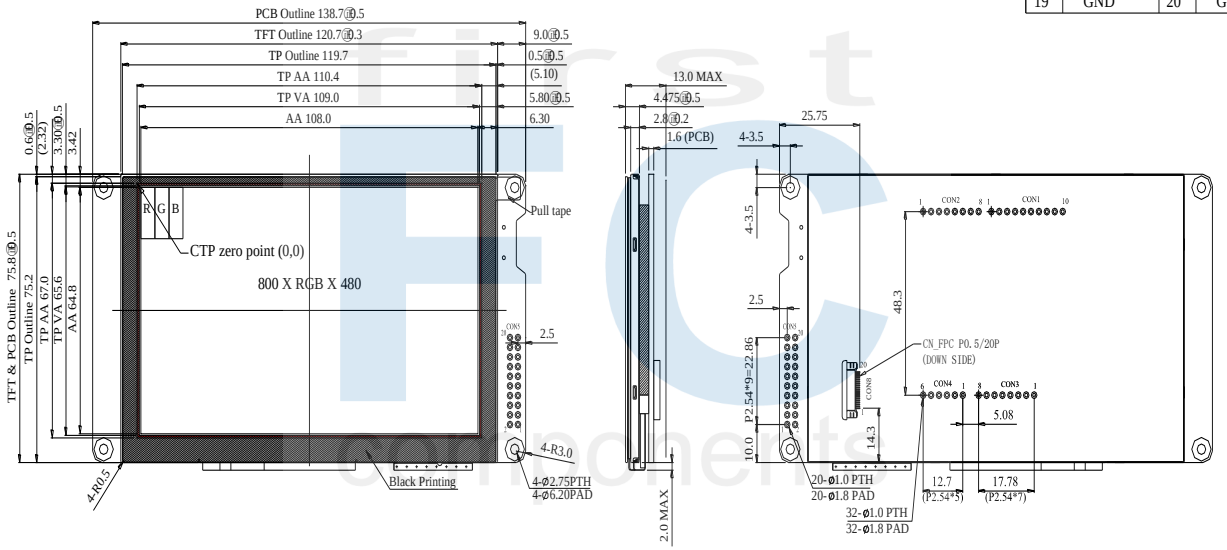
Side view

Back view

PIN No.	Pin Name
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	RST
8	NC
9	INT
10	VSS

Contour Drawing

CON5 / CON8			
PIN	SYMBOL	PIN	SYMBOL
1	VDD	2	GND
3	SCK	4	MISO
5	MOSI	6	CS
7	INT	8	PD_N
9	GPIO2	10	GPIO3
11	IO2	12	IO3
13	CSCL	14	CSDA
15	CRST	16	CINT
17	VLED	18	VLED
19	GND	20	GND



The non-specified tolerance of dimension is ±0.3 mm .