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NEXT VISION DISPLAY

SPECIFICATION

CUSTOMER : _____

MODULE NO.: NT101-N001DL

Version	1.0
Engineer	
Date	2014-12-08

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Customer:
Approved by

NEXT VISION DISPLAY CO., LTD.

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1. Scope

This data sheet is to introduce the specification of NT101-N001DL active matrix TFT module.
It is composed of a color TFT-LCD panel, driver ICs, FPC and backlight unit.
The 10,1'' display area contains 1024x(RGB)x600 pixels.

2. Application

Digital equipments which need color display, mobile navigator/video systems, tablet/notebook.

3. General Information

Item	Contents	Unit
Size	10.1	inch
Resolution	1024 (RGB) x 600	/
Interface	LVDS	/
Technology type	a-Si TFT	/
Pixel pitch	0.2175x0.2088	mm
Pixel Configuration	RGB stripes	
Outline Dimension (W x H x D)	235.0x143.0x5.0	mm
Active Area	222.72 x 125.28	mm
Display Mode	Normally white, Transmissive	/
Backlight Type	LED	/

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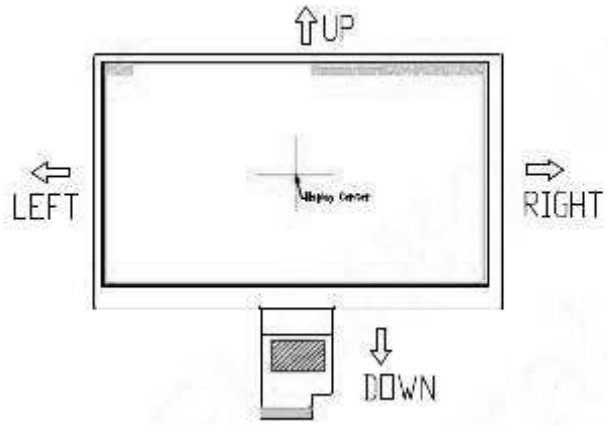
4. Interface signals

No	Symbol	Description	Remarks
1	VCOM	Common Voltage	
2	VDD	Power Voltage for digital circuit	
3	VDD	Power Voltage for digital circuit	
4	NC	No connection	
5	REST	Global reset pin	
6	STBYB	Standby mode, Normally pulled high STBYB="1",Normally operation STBYB="0",Timing controller, source driver will turn off, all output are High-Z	
7	GND	Ground	
8	RXIN0-	- LVDS differential data input	
9	RXIN0+	+LVDS differential data input	
10	GND	Ground	
11	RXIN1-	-LVDS differential data input	
12	RXIN1+	+LVDS differential data input	
13	GND	Ground	
14	RXIN2-	-LVDS differential data input	
15	RXIN2+	+LVDS differential data input	
16	GND	Ground	
17	RXCLK-	-LVDS differential clock input	
18	RXCLK+	+LVDS differential clock input	
19	GND	Ground	
20	RXIN3-	-LVDS differential data input	
21	RXIN3+	+LVDS differential data input	
22	GND	Ground	
23	NC	No connection	
24	NC	No connection	
25	GND	Ground	
26	NC	No connection	
27	DIMO/NC	Backlight CABC controller signal output/No connection	
28	SELB	6-bit / 8-bit mode select	Note 2
29	AVDD	Power for Analog Circuit	
30	GND	Ground	
31	LED-	LED Cathode	
32	LED-	LED Cathode	

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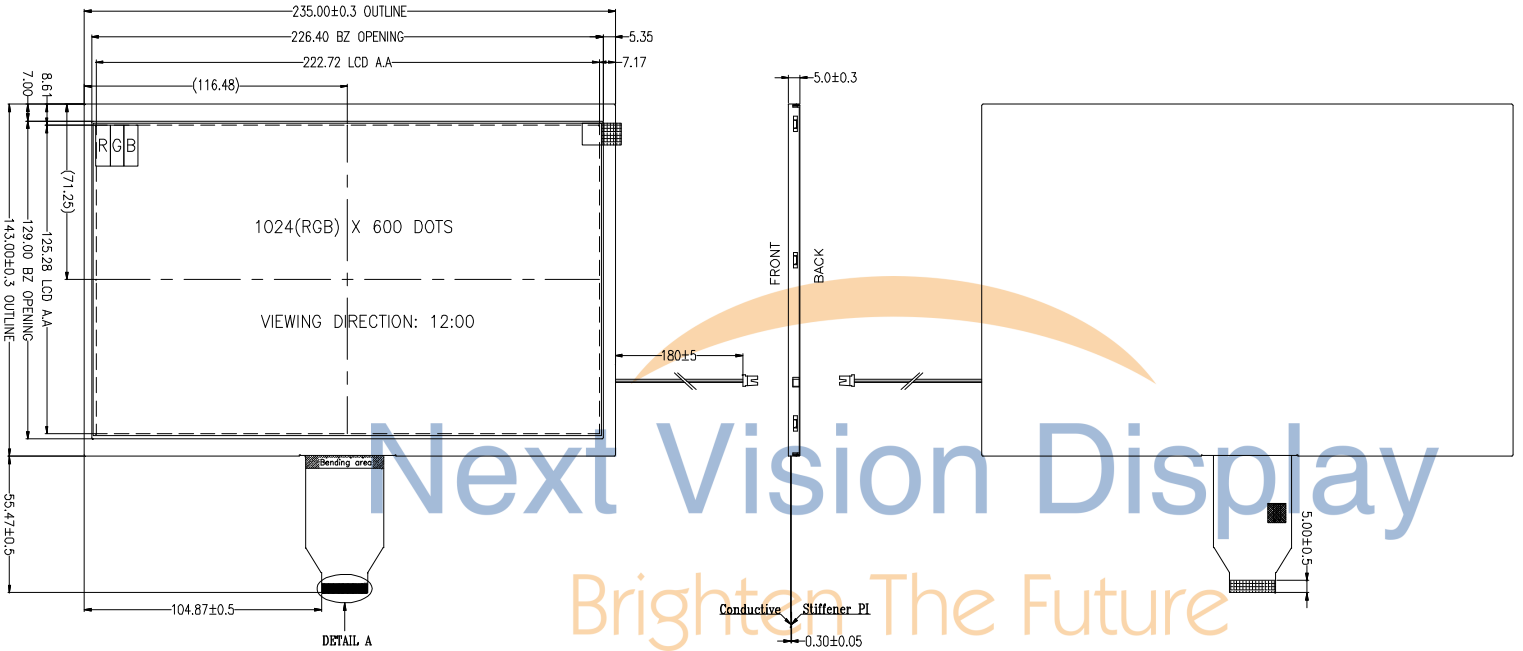
33	L/R	Horizontal inversion	Note 4
34	U/D	Vertical inversion	Note 4
35	VGL	Gate off voltage	
36	CABCEN1/NC	CABC H/W enable/No connection	Note 3
37	CABCEN0/NC	CABC H/W enable/No connection	Note 3
38	VGH	Gate ON Voltage	
39	LED+	LED Anode	
40	LED+	LED Anode	

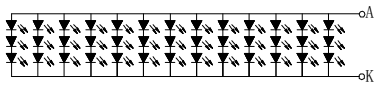
Note 1: The recommended FPC connector model is FH12A-40S-0.5SH manufactured by Hirose.
Note 2: If LVDS input data is 6 bits, SELB must be set to High;
If LVDS input data is 8 bits, SELB must be set to Low.
Note 3: When CABC_EN="00", CABC OFF.
When CABC_EN="01", user interface image.
When CABC_EN="10", still picture.
When CABC_EN="11", moving image.
When CABC off, don't connect DIMO, else connect it to backlight.
Note 4: When L/R="0", set right to left scan direction.
When L/R="1", set left to right scan direction.
When U/D="0", set top to bottom scan direction.
When U/D="1", set bottom to top scan direction.
Note 5: Definition of scanning direction
Refer to the figure as below:



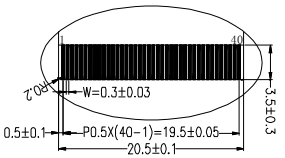
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5. Outline Drawing





Backlight LED Circuit



DETAIL A(4:1)

PIN FUCTION	
PIN	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	REST
6	STBYB
7	GND
8	Rxin0-
9	Rxin0+
10	GND
11	Rxin1-
12	Rxin1+
13	GND
14	Rxin2-
15	Rxin2+
16	GND
17	RxCLK-
18	RxCLK+
19	GND
20	Rxin3-
21	Rxin3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	DIMO/NC
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	L/R
34	U/D
35	VGL
36	CABCEN1/NC
37	CABCEN0/NC
38	VGH
39	LED+
40	LED+

NOTES:

1. DISPLAY TYPE: TFT
2. VIEWING DIRECTION: 12:00
3. LCD DRIVE IC:Source HX8282-A14 Gate:HX8696-A
4. POLARIZER MODE: TRANSMISSIVE/POSITIVE
5. VDD : 3.3V
6. BACKLIGHT: 39 PCS LEDS
7. OPERATING TEMP: 0° C~+50° C
8. STORAGE TEMP: -20° C~+60° C
9. UNMARKER TOLERANCE: ±0.20
   UNMARKED FILLET RADIUS: R=0.50
10. REQUIREMENTS ON ENVIRONMENTAL PROTECTION: RoHS

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NT101-N001DL

DRAWN BY:			
CHECKED BY:		DWG NO:	SCALE:
APPROVED BY:			UNIT: mm
CONFIRMED BY:		DWG NAME:	SHEET NO: OF