TFT DISPLAY SPECIFICATION



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Represented by:

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SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF70-1024600D5L#0SPW0-FC

APPROVED BY: **(FOR CUSTOMER USE ONLY)**	**PCB VERSION:** **DATA:**

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
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ISSUED DATE: 2025/04/23			

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General Specifications

Item	Dimension	Unit
Size	7.0	inch
Dot Matrix	1024 x RGB x 600(TFT)	dots
Module dimension	169.9(W) x 103.4(H) x5.6 (D)	mm
Active area	154.2144 x 85.92	mm
Pixel pitch	0.1506x 0.1432	mm
LCD type	TFT, Normally Black, Transmissive	
View Angle	80/80/80/80	
TFT Driver IC	JD9165BA or Equivalent	
Backlight Type	LED,Normally White	
TFT Interface	LVDS	
With /Without TP	Without TP	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-30	—	+85	℃
Storage Temperature	TST	-40	—	+90	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. ≦60℃, 90% RH MAX. Temp. > 60℃, Absolute humidity shall be less than 90% RH at 60℃

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Electrical Characteristics

Operating conditions:
Typical Operation Conditions

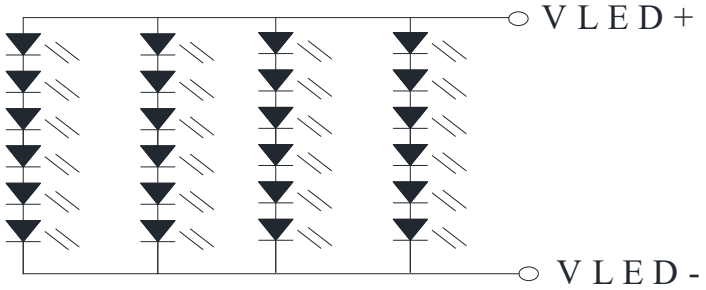
Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	VDD	3.0	3.3	3.6	V	
Analog Power	AVDD	-	11	-	V	
TFT Gate ON Voltage	VGH	19.5	20	20	V	Note1
TFT Gate OFF Voltage	VGL	-8.5	-8	-7.5	V	Note2
TFT Common Voltage	Vcom	-	4.9	-	V	Note3
Power Current	IDD	-	18	27	mA	VDD=3.3V
Analog Power Current	IAVDD	-	20.94	-	mA	AVDD=11V
TFT Gate ON Current	IVGH	-	1.5	-	mA	VGH=20V
TFT Gate OFF Current	IVGL	-	1.5	-	mA	VGL=-8V
TFT Common Current	IVCOM	-	1	-	uA	VCOM=4.9V

Note 1. VGH is TFT Gate operating Voltage.
Note 2. VGL is TFT Gate operating Voltage.
The storage structure of this model is CST (Storage on Common)
Note 3. Vcom must be adjusted to optimize display quality Crosstalk, Contrast Ratio and etc.

LED driving conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
LED current	—	—	240	—	mA	-
LED voltage	VLED+	16.2	19.2	21.0	V	Note 1
LED Life Time	—	50,000	—	—	Hr	Note 2,3,4

Note 1 : There are 1 Groups LED



B/L CIRCUIT DIAGRAM

Note 2 : Ta = 25 ℃
Note 3 : Brightness to be decreased to 50% of the initial value
Note 4 : The single LED lamp case

Optical Characteristics

TFT LCD characteristic

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr+ Tf	θ=0°、Φ=0°	-	30	35	.ms	Note 3
Contrast ratio		CR	At optimized viewing angle	700	1000	-	-	Note 4
Color Chromaticity	White	Wx	θ=0°、Φ=0	0.264	0.314	0.364	-	Note 2,6,7
		Wy		0.272	0.322	0.372	-	
Viewing angle	Hor.	ΘR	CR≧10	70	80	-	Deg.	Note 1
		ΘL		70	80	-		
	Ver.	ΦT		70	80	-		
		ΦB		70	80	-		
Brightness		-	-	1200	1300	-	cd/m²	Center of display
Uniformity		(U)	-	75	-	-	%	Note 5

Ta=25±2℃,

Note 1: Definition of viewing angle range

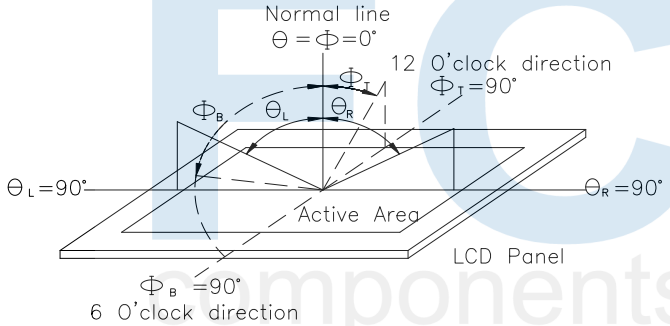


Fig.8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.

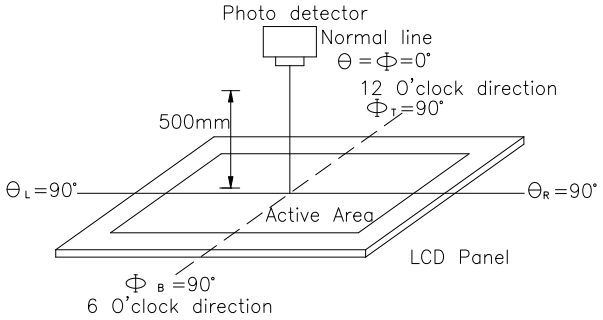


Fig. 8.2. Optical measurement system setup

Interface

LCM PIN Definition

Pin No.	Symbol	Function	Remark
1	VCOM	Common Voltage	
2	VDD	Digital circuit	
3	VDD	Digital circuit	
4	NC	No connection	
5	RESET	Global reset pin	
6	STBYB	Standby mode, Normally pulled high STBYB = “1”, normal operation STBYB = “0”, timing controller, source driver will turn off, all output are High-Z	
7	GND	Ground	
8	RXIN0-	Negative LVDS differential data input	
9	RXIN0+	Positive LVDS differential data input	
10	GND	Ground	
11	RXIN1-	Negative LVDS differential data input	
12	RXIN1+	Positive LVDS differential data input	
13	GND	Ground	
14	RXIN2-	Negative LVDS differential data input	
15	RXIN2+	Positive LVDS differential data input	
16	GND	Ground	
17	RXCLKIN-	Negative LVDS differential clock input	
18	RXCLKIN+	Positive LVDS differential clock input	
19	GND	Ground	
20	RXIN3-	Negative LVDS differential data input	
21	RXIN3+	Positive LVDS differential data input	
22	GND	Ground	
23	NC	No connection	
24	NC	No connection	
25	GND	Ground	
26	NC	No connection	
27	NC	No connection	
28	SELB	6bit/8bit mode select H:6bit / L:8bit	
29	AVDD	Power for Analog Circuit	

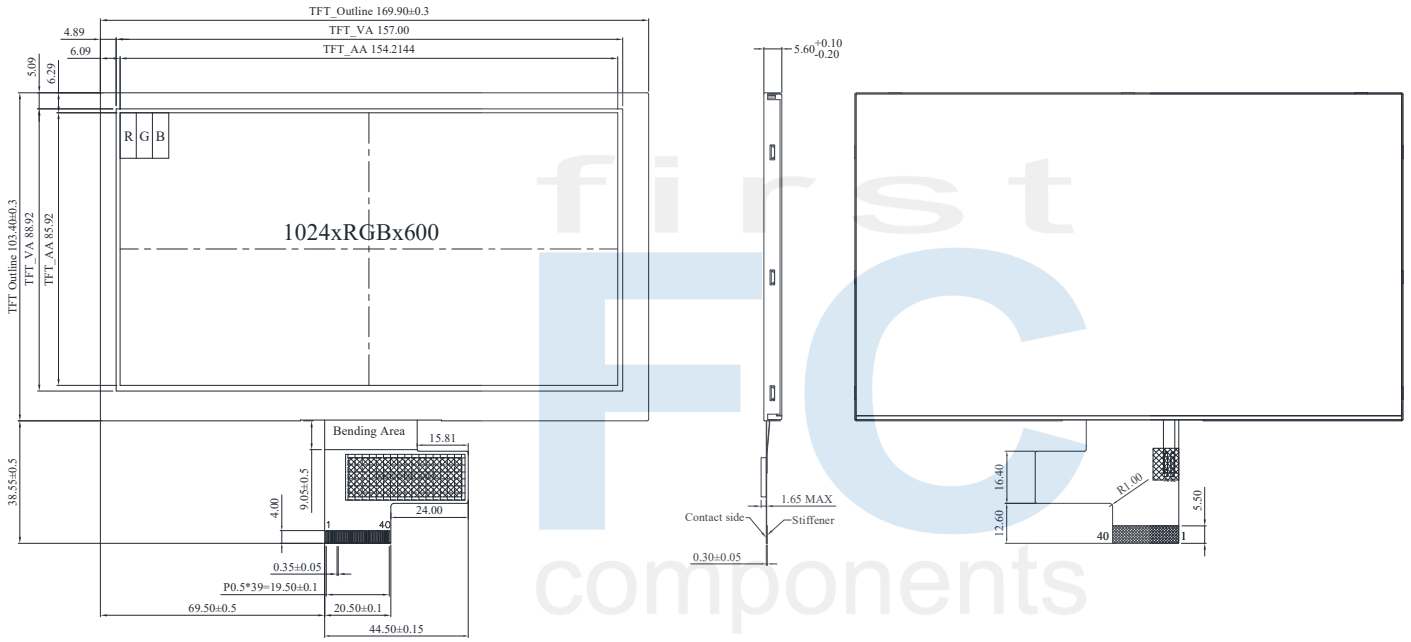
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30	GND	Ground	
31	VLED-	LED_ Cathode	
32	VLED-	LED_ Cathode	
33	L/R	Horizontal inversion	Note
34	U/D	Vertical inversion	Note
35	VGL	Negative power for TFT	
36	NC	No connection	
37	NC	No connection	
38	VGH	Positive power for TFT	
39	VLED+	LED_ Anode	
40	VLED+	LED_ Anode	

Note
When L/R="0",set right to left scan direction.
When L/R="1",set left to right scan direction.
When U/D="0",set top to bottom scan direction.
When U/D="1",set bottom to top scan direction

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Contour Drawing



PIN NO	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	NC
28	SELB
29	AVDD
30	GND
31	VLED-
32	VLED-
33	L/R
34	U/D
35	VGL
36	NC
37	NC
38	VGH
39	VLED+
40	VLED+

The non-specified tolerance of dimension is ±0.3mm.

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