Winstar Display Co., LTD
華凌光電股份有限公司
WEB: https://www.winstar.com.tw E-mail: sales@winstar.com.tw

SPECIFICATION

CUSTOMER : _____

MODULE NO.: WF70-1024600A8L#0P52-FC

APPROVED BY: **(FOR CUSTOMER USE ONLY)**	**PCB VERSION:** **DATA:**

SALES BY	APPROVED BY	CHECKED BY	PREPARED BY
first FC components			周园园
ISSUED DATE: 2022/07/05			

General Specifications

Item	Dimension	Unit
Size	7.0	inch
Dot Matrix	1024 x RGBx600(TFT)	dots
Module dimension	169.9(W) x 103.4(H) x 8.0(D)	mm
Active area	154.2144 x 85.92	mm
Dot pitch	0.1506 x 0.1432	mm
LCD type	TFT, Normally Black, Transmissive	
Viewing Angle	85/85/85/85	
TFT Interface	LVDS	
Aspect Ratio	16:9	
Driver IC	ST5021 + ST5651 or equivalent	
Backlight Type	LED, Normally White	
CTP IC	N/A or equivalent	
CTP Interface	I2C	
CTP Checksum	0x9E1088	
CTP Resolution	1024*600	
Support touch point	Contact ：five-finger Hover：One-finger , Hover high：10mm	
CTP Active Area	145.22*77.19 mm (LENS VA One-sided retraction 5mm)	
With /Without TP	With CTP	
Surface	Glare	

*Color tone slight changed by temperature and driving voltage.

Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Note: Device is subject to be damaged permanently if stresses beyond those absolute maximum ratings listed above

1. Temp. 60℃, 90% RH MAX. Temp.＞60℃, Absolute humidity shall be less than 90% RH at 60℃

Electrical Characteristics

1. Typical Operation Conditions

Item	Symbol	Values			Unit	Remark
		Min.	Typ.	Max.		
Power voltage	VDD	3.0	3.3	3.6	V	
Supply CTP	VDDT	3.0	3.3	3.6	V	
	I_{CTP}	—	TBD	TBD	mA	
Analog Power	AVDD	8.9	9.0	9.1	V	
TFT Gate ON Voltage	VGH	17	18	19	V	Note1
TFT Gate OFF Voltage	VGL	-6.5	-6.0	-5.5	V	Note2
TFT Common Voltage	Vcom	3.0	3.15	3.3	V	Note3
Current for Driver	IDD	-	14	21	mA	VDD=3.3V
Power Current	IAVDD	-	25	-	mA	AVDD=9V
TFT Gate ON Current	IVGH	-	1	-	mA	VGH=18V
TFT Gate OFF Current	IVGL	-	1	-	mA	VGL=6V
TFT Common Current	IVCOM	-	1	-	mA	VCOM=3.15V

Note:

Note 1. VGH is TFT Gate operating Voltage.

Note 2. VGL is TFT Gate operating Voltage.

The storage structure of this model is CST (Storage on Common)

Note 3. Vcom must be adjusted to optimize display quality Crosstalk, Contrast Ratio and etc.

Optical Characteristics

Item		Symbol	Condition.	Min	Typ.	Max.	Unit	Remark
Response time		Tr	θ=0°、Φ=0°	-	13	20	.ms	Note 3
		Tf		-	15	25		
Contrast ratio		CR	At optimized viewing angle	600	800	-	-	Note 4
Color Chromaticity	White	Wx	θ=0°、Φ=0	0.269	0.319	0.369	-	Note 2,5,6
		Wy		0.291	0.341	0.391	-	
Viewing angle	Hor.	ΘR	CR≧10	80	85	-	Deg.	Note 1
		ΘL		80	85	-		
	Ver.	ΦT		80	85	-		
		ΦB		80	85	-		
Brightness		-	-	400	450	-	cd/m2	Center of display
Uniformity		(U)	-	75	-	-	%	Note 5

Ta=25±2℃,

Note 1: Definition of viewing angle range

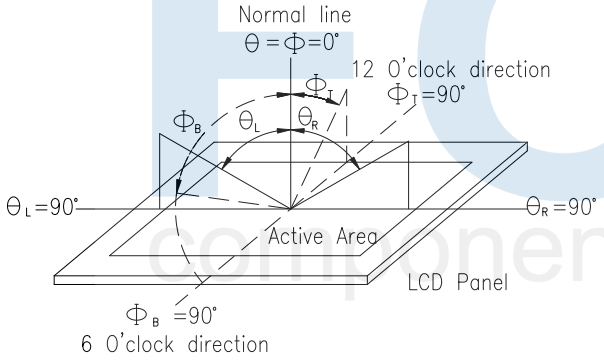
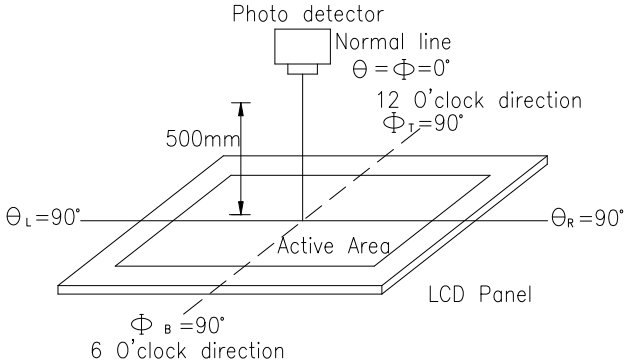


Fig. 8.1. Definition of viewing angle

Note 2: Test equipment setup:

After stabilizing and leaving the panel alone at a driven temperature for 10 minutes, the measurement should be executed. Measurement should be executed in a stable, windless, and dark room. Optical specifications are measured by Topcon BM-7orBM-5 luminance meter 1.0° field of view at a distance of 50cm and normal direction.



Interface

1. LCM PIN Definition

Pin No.	Symbol	I/O	Function	Remark
1	VCOM	P	Common Voltage	
2	VDD	P	Digital circuit	
3	VDD	P	Digital circuit	
4	NC	---	No connection	
5	RESET	I	Global reset pin	
6	STBYB	I	Standby mode, Normally pulled high STBYB = “1”, normal operation STBYB = “0”, timing controller, source driver will turn off, all output are High-Z	
7	GND	P	Ground	
8	RXIN0-	I	Negative LVDS differential data input	
9	RXIN0+	I	Positive LVDS differential data input	
10	GND	P	Ground	
11	RXIN1-	I	Negative LVDS differential data input	
12	RXIN1+	I	Positive LVDS differential data input	
13	GND	P	Ground	
14	RXIN2-	I	Negative LVDS differential data input	
15	RXIN2+	I	Positive LVDS differential data input	
16	GND	P	Ground	
17	RXCLKIN-	I	Negative LVDS differential clock input	
18	RXCLKIN+	I	Positive LVDS differential clock input	
19	GND	P	Ground	
20	RXIN3-	I	Negative LVDS differential data input	
21	RXIN3+	I	Positive LVDS differential data input	
22	GND	P	Ground	
23	NC	---	No connection	
24	NC	---	No connection	
25	GND	P	Ground	
26	NC	---	No connection	
27	NC	---	No connection	
28	SELB	I	6bit/8bit mode select H:6bit / L:8bit	
29	AVDD	P	Power for Analog Circuit	

first components

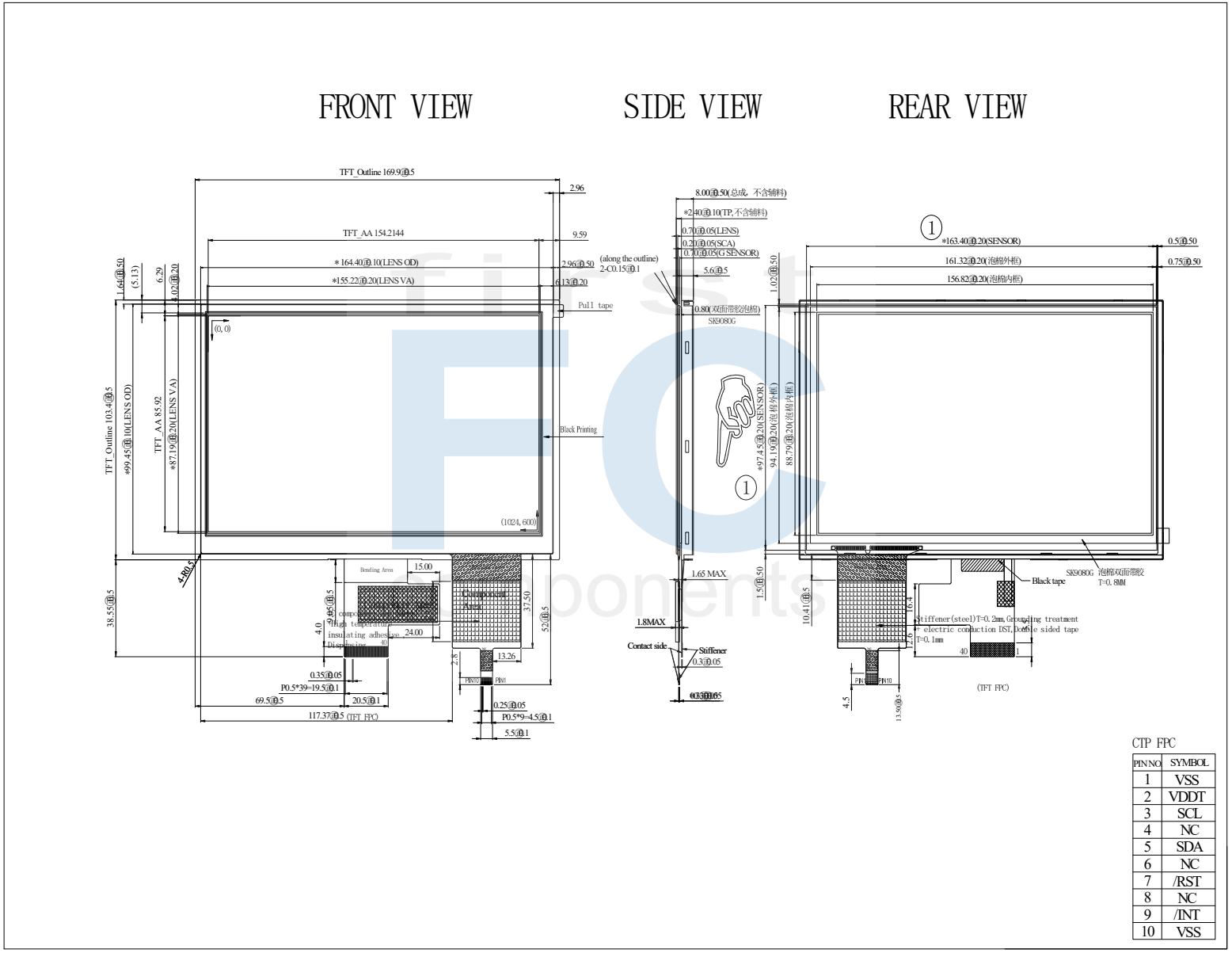
30	GND	P	Ground	
31	LED-	P	LED Cathode	
32	LED-	P	LED Cathode	
33	L/R	I	Horizontal inversion	
34	U/D	I	Vertical inversion	
35	VGL	P	Negative power for TFT	
36	NC	---	No connection	
37	NC	---	No connection	
38	VGH	P	Positive power for TFT	
39	LED+	P	LED Anode	
40	LED+	P	LED Anode	

I:input ,O:output,P:power
Note:
When L/R="0",set right to left scan direction.
When L/R="1",set left to right scan direction.
When U/D="0",set top to bottom scan direction.
When U/D="1",set bottom to top scan direction.

9.2. CTP PIN Definition

Pin	Symbol	Function	Remark
1	VSS	Ground for analog circuit	
2	VDDT	Power Supply : +3.3V	
3	SCL	I2C clock input	
4	NC	No connect	
5	SDA	I2C data input and output	
6	NC	No connect	
7	/RST	External Reset, Low is active	
8	NC	No connect	
9	/INT	External interrupt to the host	
10	VSS	Ground for analog circuit	

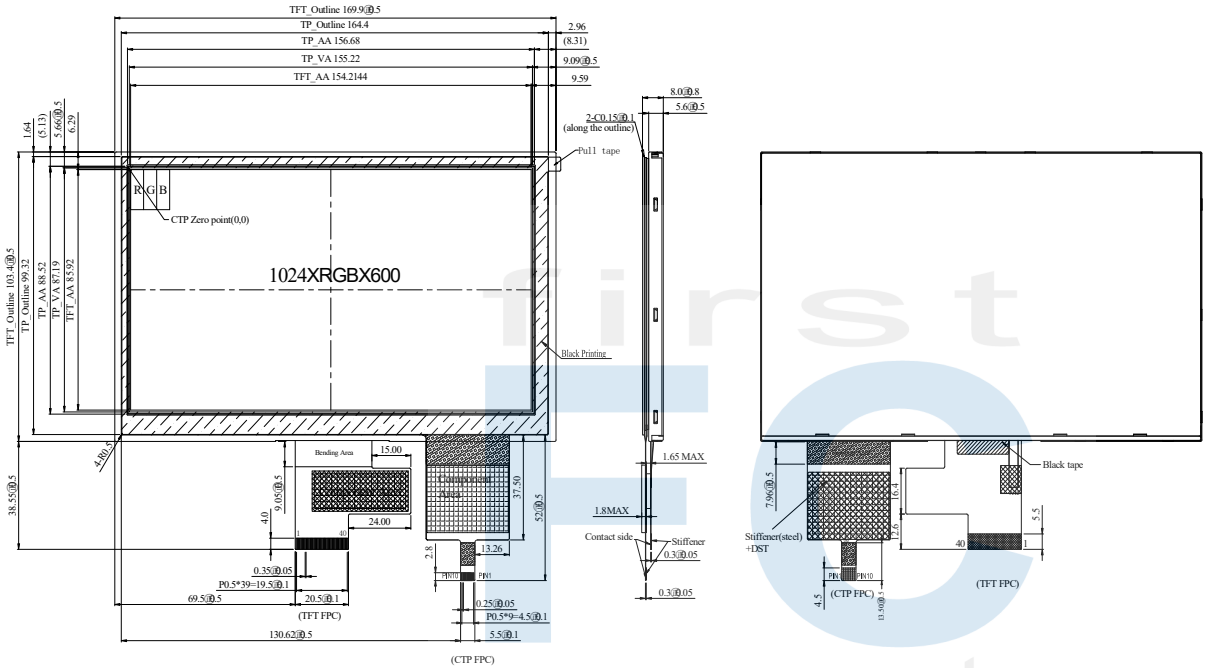
Touch Panel Information



CTP FPC

PIN NO	SYMBOL
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

Contour Drawing



CTP FPC

PIN NO.	SYMBOL
1	VSS
2	VDDT
3	SCL
4	NC
5	SDA
6	NC
7	/RST
8	NC
9	/INT
10	VSS

TFT FPC

PIN NO.	SYMBOL
1	VCOM
2	VDD
3	VDD
4	NC
5	RESET
6	STBYB
7	GND
8	RXIN0-
9	RXIN0+
10	GND
11	RXIN1-
12	RXIN1+
13	GND
14	RXIN2-
15	RXIN2+
16	GND
17	RXCLKIN-
18	RXCLKIN+
19	GND
20	RXIN3-
21	RXIN3+
22	GND
23	NC
24	NC
25	GND
26	NC
27	NC
28	SELB
29	AVDD
30	GND
31	LED-
32	LED-
33	L/R
34	U/D
35	VGL
36	NC
37	NC
38	VGH
39	LED+
40	LED+

The non-specified tolerance of dimension is ±0.3mm.

first components