TFT DISPLAY SPECIFICATION



WINSTAR Display Co.,Ltd.

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SPECIFICATION

MODULE NO.: WF70-8001280D4M#0SP0-FC

General Specifications

Item	Dimension	Unit
Size	7.0	inch
Dot Matrix	800× RGB ×1280	dots
Module dimension	104.50 x 161.80 x 5.1	mm
Active area	94.2 x 150.72	mm
Pixel pitch	0.11775 x 0.11775	mm
LCD type	TFT, Normally Black, Transmissive	
Viewing angle	85/85/85/85	
TFT Driver IC	ILI9881C or Equivalent	
TFT Interface	2-Lanes MIPI	
Aspect Ratio	16:9	
Backlight Type	LED ,Normally White	
Touch Panel	Without Touch Panel	
Surface	Anti-Glare	

*Color tone slight changed by temperature and driving voltage.

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Absolute Maximum Ratings

Item	Symbol	Min	Typ	Max	Unit
Operating Temperature	TOP	-20	—	+70	℃
Storage Temperature	TST	-30	—	+80	℃

Electrical Characteristics

1. Typical Operation Conditions

Item	Symbol	Values			Unit
		Min.	Typ.	Max.	
Supply voltage	VCI	2.5	3.3	3.6	V
	IOVCC	1.7	1.8	1.9	V
Supply LCM current	I_{VCI}	-	59	90	mA
	I_{IOVCC}	-	18	30	mA

2. Backlight Driving Conditions

Parameter	Symbol	Min.	Typ.	Max.	Unit
LED current	ILED	-	140	-	mA
LED voltage	VLED+	16.8	18.0	19.2	V
LED Life Time		-	30,000	-	Hr

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Interface

1. LCM PIN Definition

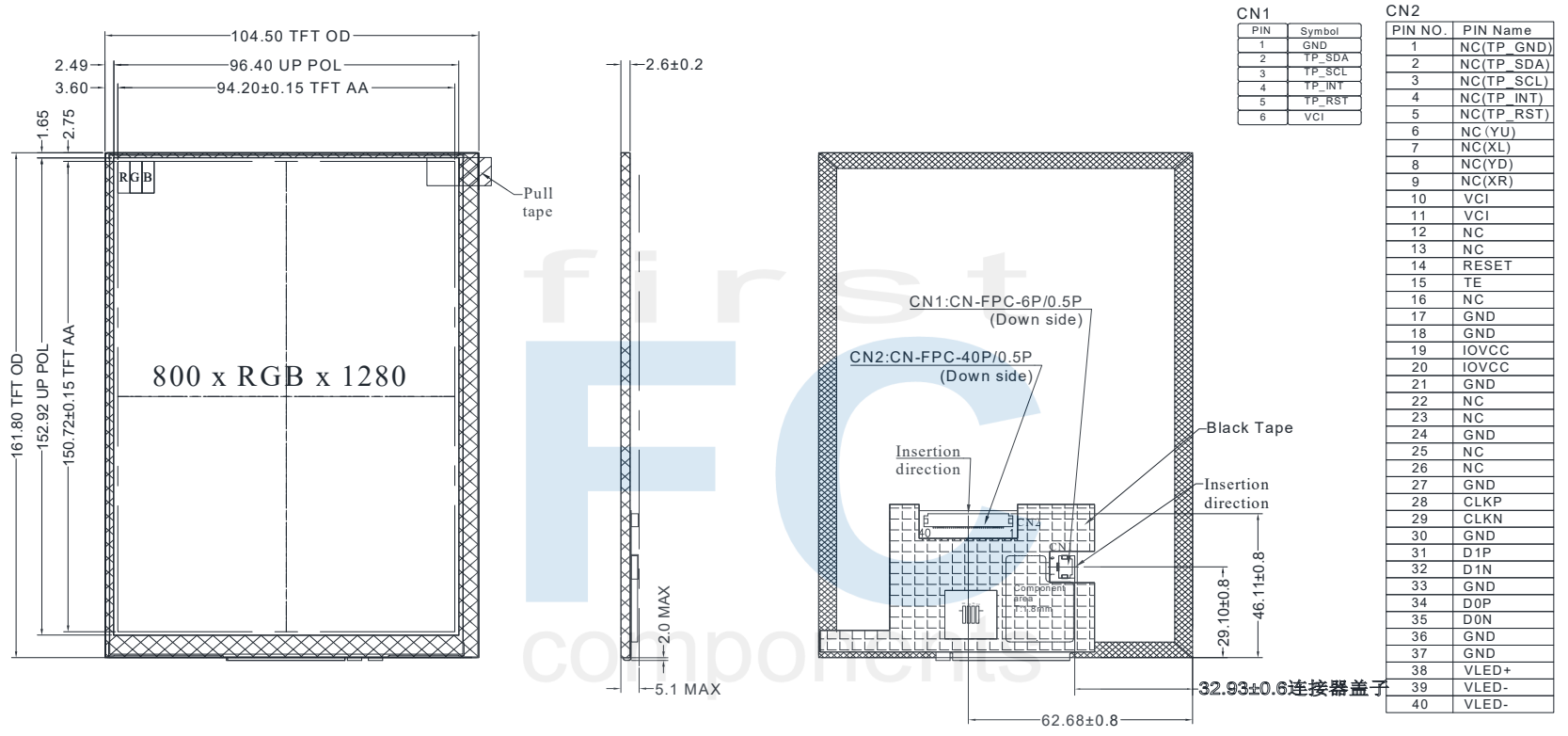
Pin	Symbol	Function
1	NC(TP_GND)	No connection
2	NC(TP_SDA)	No connection
3	NC(TP_SCL)	No connection
4	NC(TP_INT)	No connection
5	NC(TP_RST)	No connection
6	NC(YU)	No connection
7	NC(XL)	No connection
8	NC(YD)	No connection
9	NC(XR)	No connection
10-11	VCI	Power for analog circuit.
12-13	NC	No connection
14	RESET	Global reset pin.
15	TE	Tearing effect output pin. Leave the pin open when not in use.
16	NC	No connection
17-18	GND	Ground
19-20	IOVCC	Power supply for I/O system.
21	GND	Ground
22-23	NC	No connection
24	GND	Ground
25-26	NC	No connection
27	GND	Ground
28	CLKP	MIPI DSI differential clock pair leave it open or fix to LVDSVSS level when not in use
29	CLKN	MIPI DSI differential clock pair leave it open or fix to LVDSVSS level when not in use
30	GND	Ground
31	D1P	MIPI DSI differential data pair.
32	D1N	MIPI DSI differential data pair.
33	GND	Ground
34	D0P	MIPI DSI differential data pair.
35	D0N	MIPI DSI differential data pair.

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36-37	GND	Ground
38	VLED+	LED input terminal (Anode)
39-40	VLED-	Ground (Cathode)

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Contour Drawing



CN1

PIN	Symbol
1	GND
2	TP_SDA
3	TP_SCL
4	TP_INT
5	TP_RST
6	VCI

CN2

PIN NO.	PIN Name
1	NC(TP_GND)
2	NC(TP_SDA)
3	NC(TP_SCL)
4	NC(TP_INT)
5	NC(TP_RST)
6	NC(YU)
7	NC(XL)
8	NC(YD)
9	NC(XR)
10	VCI
11	VCI
12	NC
13	NC
14	RESET
15	TE
16	NC
17	GND
18	GND
19	IOVCC
20	IOVCC
21	GND
22	NC
23	NC
24	GND
25	NC
26	NC
27	GND
28	CLKP
29	CLKN
30	GND
31	D1P
32	D1N
33	GND
34	D0P
35	D0N
36	GND
37	GND
38	VLED+
39	VLED-
40	VLED-

The non-specified tolerance of dimension is ±0.3 mm .

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