VGG243264-7UFLWA-FC

GENERAL SPECIFICATION

ITEM	CONTENTS
Module Size	50.0 (W)mm * 98.95 (H)mm * 4.2(D) mm
Display Size	2.8 inch
Display Format	240(RGB)* 320 Pixels
Active Area	43.2 (W) * 57.6 (H) mm
Pixel Pitch	0.18 * 0.18 mm
LCD Type	TFT (262K)/ Transmissive / Positive
View Angle	12 O'clock (The Gray Inversion will appear at this direction)
Controller IC	ILI9325
Backlight Driver type	External Power
DC to DC circuit	Build-In
Approx. Weight	24.5g

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ABSOLUTE MAXIMUM RATING(Ta=25℃ VSS=0V)

Item	Symbol	Min.	Type	Max.	Unit	Humidity
Supply Voltage for Logic	VCC-GND	-0.3	-	+4.6	Volt	-
Supply Voltage for Analog	VCI-GND	-0.3	-	+4.6	Volt	-
Operating Temperature	Top	-20	-	+70	℃	Note1
Storage Temperature	Tst	-30	-	+80	℃	Note2

Note1: Background color changes slightly depending on ambient temperature. This phenomenon is reversible.

Ta≦70℃: 75%RH max

Ta>70℃: absolute humidity must be lower than the humidity of 75%RH at 70℃

Note2: Ta at -30℃ will be <48hrs, at 80 ℃ will be <120hrs

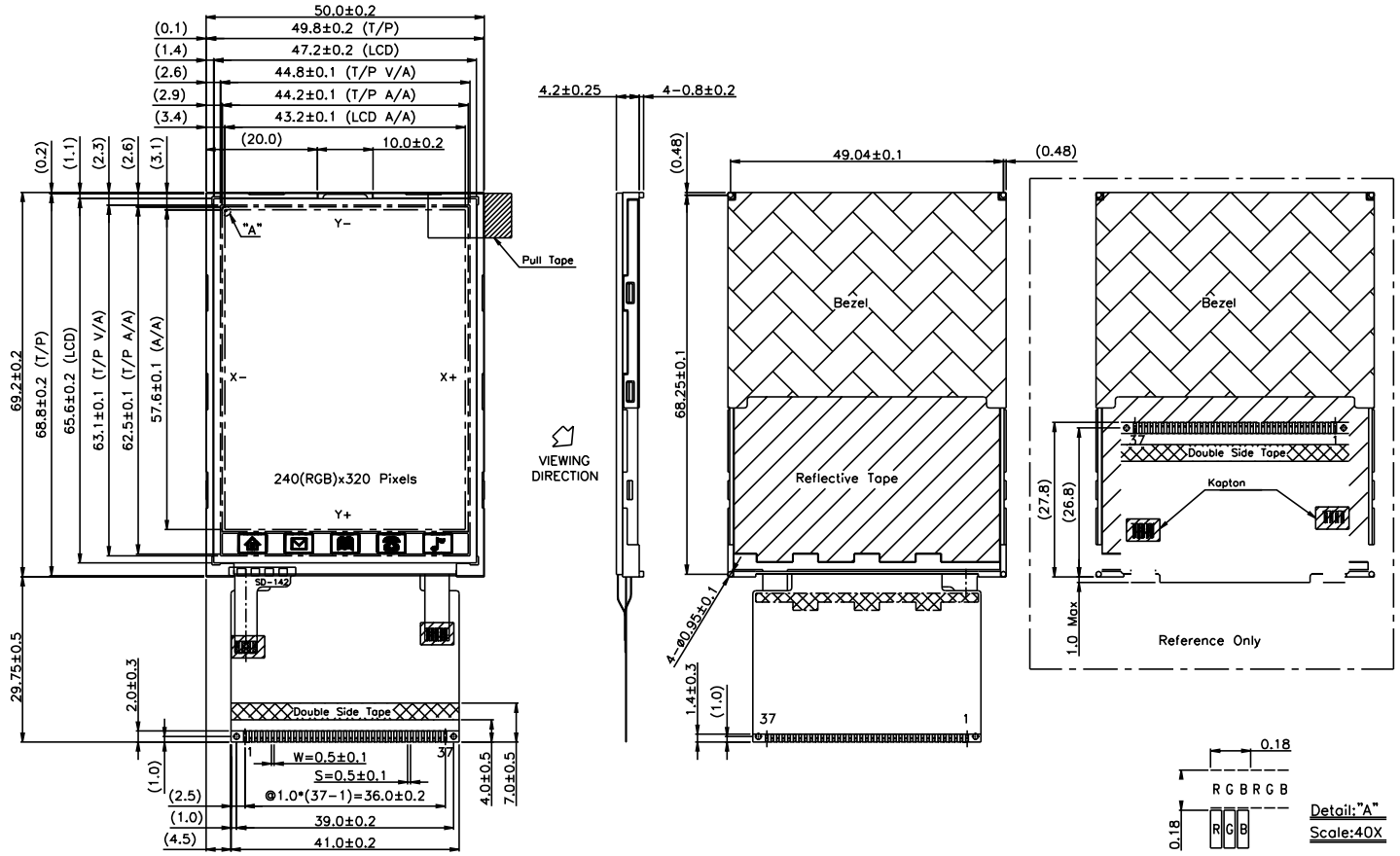
ELECTRO-OPTICAL CHARACTERISTICS (Ta=25℃)

Item	Symbol	Condition	Min.	Typ.	Max.	Unit
Power Supply for Logic	VCC-GND	-	-	2.8	-	Volt
Power Supply for Analog	VCI-GND	-	-	2.8	-	Volt
Power Supply for interface	IOVCC-GND	-	-	2.8	-	Volt
Power Supply Current for LCM	IDD	VCC=VCI= IOVCC =2.8V	-	3.8	5.4	mA

INTERFACE PIN ASSIGNMENT

<table>
<tr><th>No.</th><th>Symbol</th><th>Function</th></tr>
<tr><td>1</td><td>DB1</td><td>A data bus pin.</td></tr>
<tr><td>2</td><td>DB2</td><td>A data bus pin.</td></tr>
<tr><td>3</td><td>DB3</td><td>A data bus pin.</td></tr>
<tr><td>4</td><td>DB4</td><td>A data bus pin.</td></tr>
<tr><td>5</td><td>GND</td><td>Ground</td></tr>
<tr><td>6</td><td>VCC</td><td>A supply voltage to the internal logic.</td></tr>
<tr><td>7</td><td>nCS</td><td>A chip select signal.
Low: the ILI9325 is selected and accessible
High: the ILI9325 is not selected and not accessible</td></tr>
<tr><td>8</td><td>RS</td><td>A register select signal.
Low: select an index or status register
High: select a control register</td></tr>
<tr><td>9</td><td>nWR</td><td>A write strobe signal and enables an operation to write data when the signal is low.</td></tr>
<tr><td>10</td><td>nRD</td><td>A read strobe signal and enables an operation to read out data when the signal is low.</td></tr>
<tr><td>11</td><td>IM0</td><td>Select the MPU system interface mode.
<table>
<tr><th>IM3</th><th>IM0</th><th>MPU-Interface</th><th>DB Pin in use</th></tr>
<tr><td>0</td><td>0</td><td>I80-system 16-bit interface</td><td>DB[17:10],DB[8:1]</td></tr>
<tr><td>0</td><td>1</td><td>I80-system 8-bit interface</td><td>DB[17:10]</td></tr>
</table></td></tr>
<tr><td>12</td><td>X+</td><td>Touch panel coordinate in the left side of envisage drawing.</td></tr>
<tr><td>13</td><td>Y+</td><td>Touch panel coordinate in the up side of envisage drawing.</td></tr>
<tr><td>14</td><td>X-</td><td>Touch panel coordinate in the right side of envisage drawing.</td></tr>
<tr><td>15</td><td>Y-</td><td>Touch panel coordinate in the down side of envisage drawing.</td></tr>
<tr><td>16</td><td>LED_A</td><td>LED Anode</td></tr>
<tr><td>17</td><td>LED_K1</td><td>LED Cathode</td></tr>
<tr><td>18</td><td>LED_K2</td><td>LED Cathode</td></tr>
<tr><td>19</td><td>LED_K3</td><td>LED Cathode</td></tr>
<tr><td>20</td><td>LED_K4</td><td>LED Cathode</td></tr>
<tr><td>21</td><td>IM3</td><td>Select the MPU system interface mode.
<table>
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</table></td></tr>
</table>

No.	Symbol	Function
22	DB5	A data bus pin.
23	DB10	A data bus pin.
24	DB11	A data bus pin.
25	DB12	A data bus pin.
26	DB13	A data bus pin.
27	DB14	A data bus pin.
28	DB15	A data bus pin.
29	DB16	A data bus pin.
30	DB17	A data bus pin.
31	nRESET	A reset pin. Initializes the ILI9325 with a low input. Be sure to execute a power-on reset after supplying power.
32	VCI	A supply voltage to the analog circuit.
33	IOVCC	A voltage to the interface pins.
34	GND	Ground
35	DB6	A data bus pin.
36	DB7	A data bus pin.
37	DB8	A data bus pin.



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