WINSTAR Display

OLED SPECIFICATION

Model No:

WEO25664x#A08-FC

WINSTAR DISPLAY CO.; LTD

Represented by:

first FC components

FIRST Components srl Via Cavour 69, 21020 Buguggiate (VA)

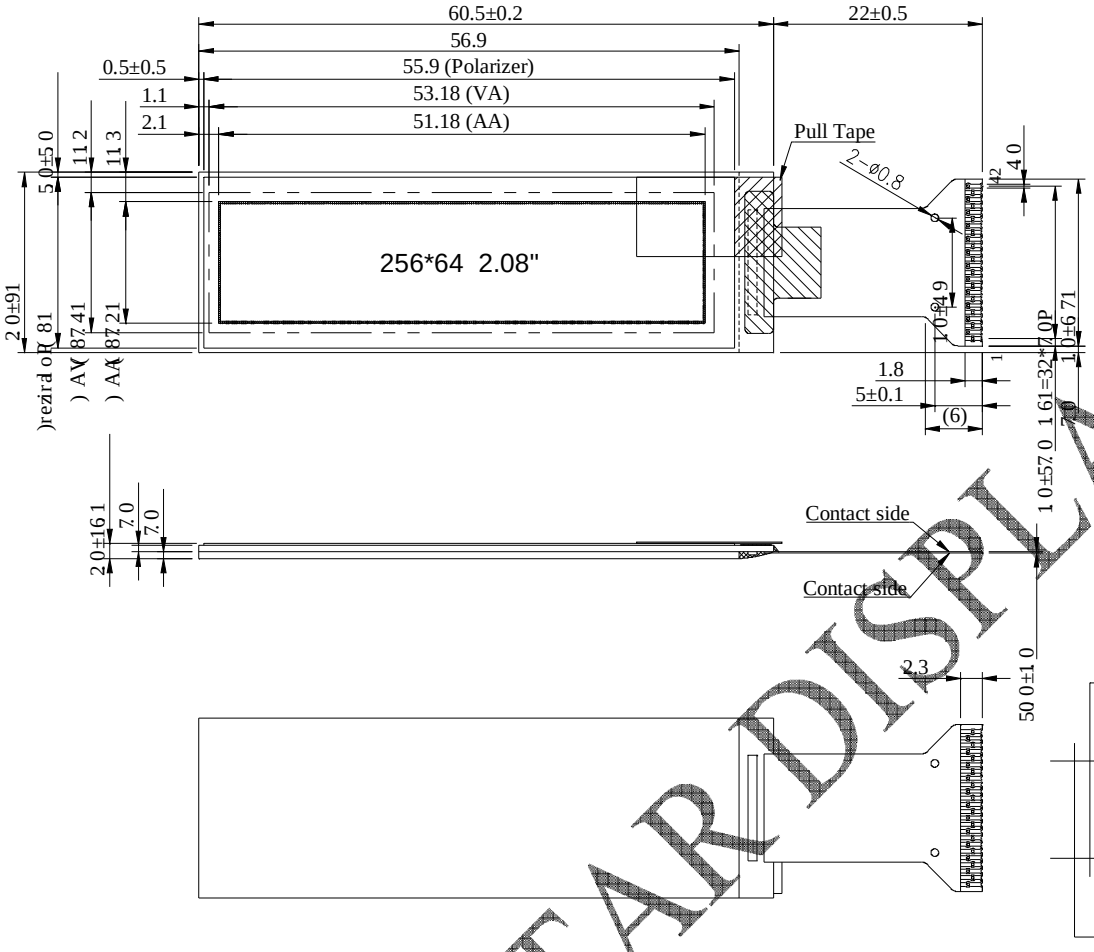
T. +39 0332 1888005 web: www.first-components.com

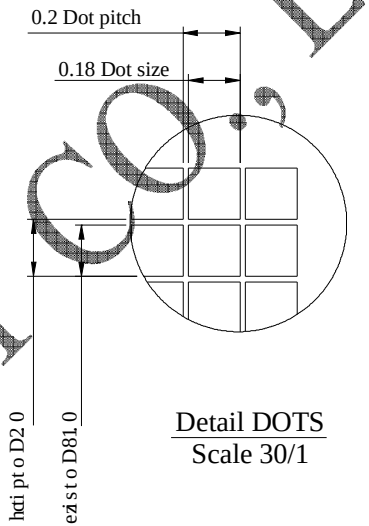
General Specification

Item	Dimension	Unit
Dot Matrix	256 x 64 Dots	—
Module dimension	60.5 x 19.0 x 1.61	mm
Active Area	51.18 x 12.78	mm
Pixel Size	0.18 x 0.18	mm
Pixel Pitch	0.2 x 0.2	mm
Display Mode	Passive Matrix	
Display Color	Monochrome	
Drive Duty	1/64 Duty	
Gray Scale	4 Bits	
IC	SSD1362	
Interface	3-Wire and 4-Wire SPI, I2C, 6800, 8080	
Size	2.08 inch	

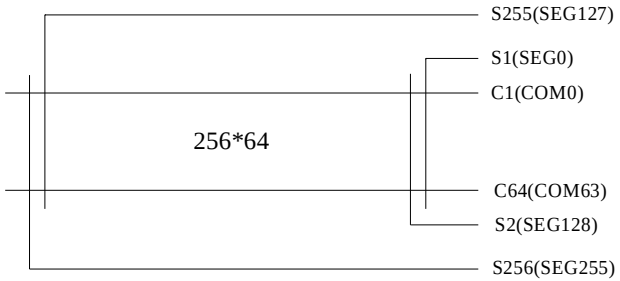
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Contour Drawing & Block Diagram





Detail DOTS
Scale 30/1



PIN	SYMBOL
1	VCC
2	VP
3	VCOMH
4	VDD
5	IREF
6	VSS
7	VSS
8	VCI
9	BS0
10	BS1
11	BS2
12	CS
13	RES
14	D/C
15	R/W
16	RD
17	D0
18	D1
19	D2
20	D3
21	D4
22	D5
23	D6
24	D7

The non-specified tolerance of dimension is ±0.3 mm .

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Interface Pin Function

<table>
<tr><th>No.</th><th>Symbol</th><th>Function</th></tr>
<tr><td>1</td><td>VCC</td><td>Power supply for panel driving voltage. This is also the most positive power voltage supply pin. It is supplied by external high voltage source.</td></tr>
<tr><td>2</td><td>VP</td><td>This pin is the segment pre-charge voltage reference pin.
A capacitor should be connected between this pin and VSS.
No external power supply is allowed to connect to this pin.</td></tr>
<tr><td>3</td><td>VCOMH</td><td>COM signal deselected voltage level.
A capacitor should be connected between this pin and VSS.
No external power supply is allowed to connect to this pin.</td></tr>
<tr><td>4</td><td>VDD</td><td>Power supply for core logic operation.
VDD can be supplied externally (within the range of 1.65V to 2.6V) or regulated internally from VCI when VCI is >2.6V.
A capacitor should be connected between VDD and VSS under all circumstances.</td></tr>
<tr><td>5</td><td>IREF</td><td>This pin is the segment output current reference pin.
When external IREF is used, a resistor should be connected between this pin and VSS to maintain current of around 18.75uA.
When internal IREF is used, this pin should be kept NC.</td></tr>
<tr><td>6</td><td>VSS</td><td>Ground pin. It must be connected to external ground.</td></tr>
<tr><td>7</td><td>VSS</td><td>Ground pin. It must be connected to external ground.</td></tr>
<tr><td>8</td><td>VCI</td><td>Low voltage power supply.
VCI must always be equal to or higher than VDD and VDDIO.</td></tr>
<tr><td>9</td><td>BS0</td><td rowspan="3">MCU bus interface selection pins. Select appropriate logic setting as described in the following table. BS2 and BS1, BS0 are pin select.
<table><tr><th>BS[2:0]</th><th>Interface</th></tr><tr><td>000</td><td>4 line SPI</td></tr><tr><td>001</td><td>3 line SPI</td></tr><tr><td>110</td><td>8-bit 8080 parallel</td></tr><tr><td>100</td><td>8-bit 6800 parallel</td></tr><tr><td>010</td><td>I²C</td></tr></table>Note (1) 0 is connected to VSS (2) 1 is connected to VCI</td></tr>
<tr><td>10</td><td>BS1</td></tr>
<tr><td>11</td><td>BS2</td></tr>
<tr><td>12</td><td>CS</td><td>This pin is the chip select input connecting to the MCU.
The chip is enabled for MCU communication only when CS is pulled LOW (active LOW).
In I2C mode, this pin must be connected to VSS.</td></tr>
<tr><td>13</td><td>RES</td><td>This pin is reset signal input.
When the pin is pulled LOW, initialization of the chip is executed.
Keep this pin pull HIGH during normal operation.</td></tr>
<tr><td>14</td><td>D/C</td><td>This pin is Data/Command control pin connecting to the MCU.
When the pin is pulled HIGH, the data at D[7:0] will be interpreted as data.
When the pin is pulled LOW, the data at D[7:0] will be transferred to a command register.
In I2C mode, this pin acts as SA0 for slave address selection.</td></tr>
</table>

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		When 3-wire serial interface is selected, this pin must be connected to VSS.
15	R/W	This pin is read / write control input pin connecting to the MCU interface. When 6800 interface mode is selected, this pin will be used as Read/Write (R/W) selection input. Read mode will be carried out when this pin is pulled HIGH and write mode when LOW. When 8080 interface mode is selected, this pin will be the Write (WR) input. Data write operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
16	RD	This pin is MCU interface input. When 6800 interface mode is selected, this pin will be used as the Enable signal. Read/write operation is initiated when this pin is pulled HIGH and the chip is selected. When 8080 interface mode is selected, this pin receives the Read (RD) signal. Read operation is initiated when this pin is pulled LOW and the chip is selected. When serial or I2C interface is selected, this pin must be connected to VSS.
17	D0	These pins are bi-directional data bus connecting to the MCU data bus. Unused pins are recommended to tie LOW. When serial interface mode is selected, D0 will be the serial clock input: SCLK; D1 will be the serial data input: SID. When I2C mode is selected, D2, D1 should be tied together and serve as SDA_{out}, SDA_{in} in application and D0 is the serial clock input, SCL.
18	D1	
19	D2	
20	D3	
21	D4	
22	D5	
23	D6	
24	D7	

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Absolute Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Supply Voltage for Operation	VCI	-0.5	5.5	V
Supply Voltage for Logic	VDD	-0.5	2.75	V
Supply Voltage for Display	VCC	-0.5	21	V
Operating Temperature	TOP	-40	+80	°C
Storage Temperature	TSTG	-40	+85	°C

Electrical Characteristics

DC Electrical Characteristics

Item	Symbol	Condition	Min	Typ	Max	Unit
Supply Voltage for Logic	VCI	—	2.8	3.0	3.3	V
Supply Voltage for Display	VCC	—	11.5	12.0	12.5	V
Input High Volt.	VIH	—	0.8×VCI	—	VCI	V
Input Low Volt.	VIL	—	VSS	—	0.2×VCI	V
Output High Volt.	VOH	—	0.9×VCI	—	VCI	V
Output Low Volt.	VOL	—	VSS	—	0.1×VCI	V
50% Check Board operating Current	ICC	VCC=12V	—	15	30	mA

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