SPECIFICATION Version: 0

CUSTOMER :

MODULE NO. : WEN25664x#BA3N00000-FC

General Specification

Item	Dimension	Unit
Dot Matrix	256 x 64 Dots	—
Module dimension	89.2 x 44.0 x 3.3 Max.	mm
Active Area	76.78 x 19.18	mm
Pixel Size	0.278 x 0.278	mm
Pixel Pitch	0.3 x 0.3	mm
Display Mode	Passive Matrix	
Display Color	Sky Blue	
Drive Duty	1/64 Duty	
Gray Scale	4 bits	
IC	SSD1322 (COF)	
Interface	6800, 8080, SPI	
Size	3.12 inch	

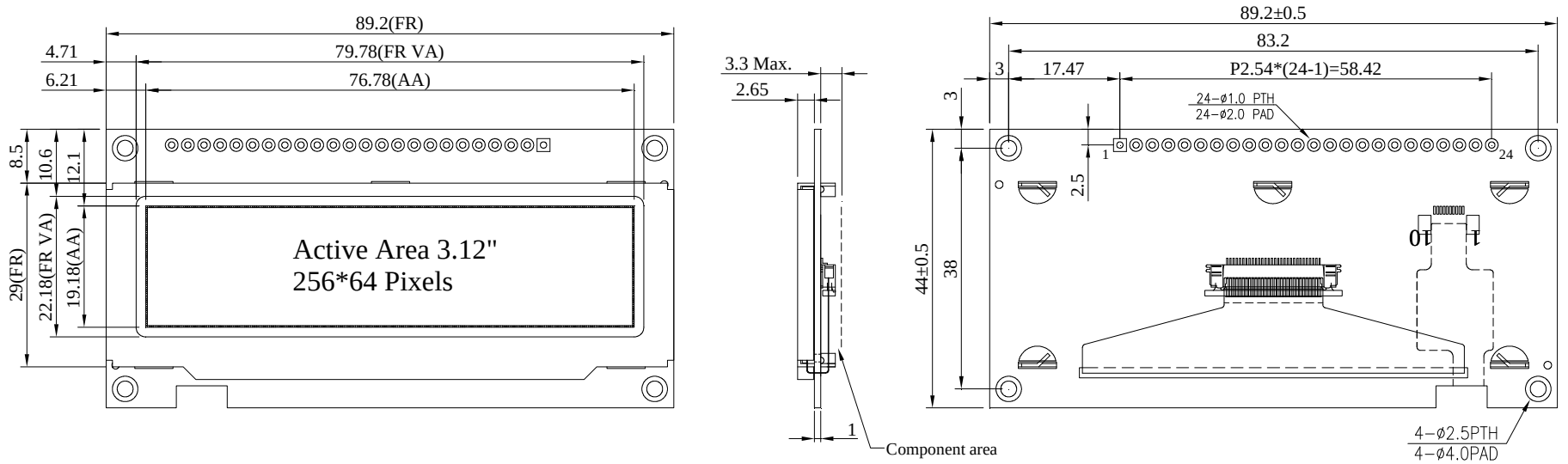
Represented by:

first FC components

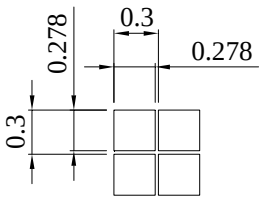
FIRST Components srl Via Cavour 69, 21020 Buguggiate (VA)

T. +39 0332 1888005 web: www.first-components.com

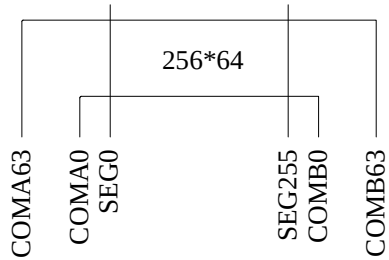
Contour Drawing & Block Diagram



PIN	SYMBOL	PIN	SYMBOL
1	VSS	13	DB6
2	VDD	14	DB7
3	NC	15	NC
4	D/C#	16	RES#
5	R/W#(WR#)	17	CS#
6	E(/RD#)	18	NC
7	DB0	19	BS1
8	DB1	20	BS0
9	DB2	21	NC
10	DB3	22	NC
11	DB4	23	NC
12	DB5	24	NC



DOT SIZE
SCALE 20/1



The non-specified tolerance of dimension is ±0.3 mm .

Interface Pin Function

Pin Number	Symbol	I/O	Function
1	VSS	P	Ground.
2	VDD	P	***Power Supply for Core Logic Circuit*** Power supply pin for core logic operation. A capacitor is required to connect between this pin and VSS
3	N.C.	P	***Reserved Pin*** The N.C. pin between function pins are reserved for compatible and flexible design.
4	D/C#	I	***Data/Command Control*** This pin is Data/Command control pin connecting to the MCU. When the pin is pulled HIGH, the content at D[7:0] will be interpreted as data. When the pin is pulled LOW, the content at D[7:0] will be interpreted as command.
5	R/W# (WR#)	I	***Read/Write Select or Write*** This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as Read/Write (R/W#) selection input. Pull this pin to “High” for read mode and pull it to “Low” for write mode. When 80XX interface mode is selected, this pin will be the Write (WR#) input. Data write operation is initiated when this pin is pulled low and the CS# is pulled low. When serial mode is selected, this pin must be connected to VSS.
6	E/RD#	I	***Read/Write Enable or Read*** This pin is MCU interface input. When interfacing to a 68XX-series microprocessor, this pin will be used as the Enable (E) signal. Read/write operation is initiated when this pin is pulled high and the CS# is pulled low. When connecting to an 80XX-microprocessor, this pin receives the Read (RD#) signal. Data read operation is initiated when this pin is pulled low and CS# is pulled low. When serial mode is selected, this pin must be connected to VSS.
7~14	DB0 DB1 DB2 DB3 DB4 DB5 DB6 DB7	I/O	***Host Data Input/Output Bus*** These pins are 8-bit bi-directional data bus to be connected to the microprocessor’s data bus. When serial mode is selected, D1 will be the serial data input SDIN and D0 will be the serial clock input SCLK.
15	NC	P	***Reserved Pin*** The N.C. pin between function pins are reserved for compatible and flexible design.

<table>
<tr><td>16</td><td>RES#</td><td>I</td><td>This pin is reset signal input.
When the pin is pulled LOW, initialization of the chip is executed.
Keep this pin pull HIGH during normal operation.</td></tr>
<tr><td>17</td><td>CS#</td><td>I</td><td>Data/Command Control
This pin is the chip select input connecting to the MCU. The chip is enabled for MCU communication only when CS# is pulled LOW.</td></tr>
<tr><td>18</td><td>NC</td><td>P</td><td>Reserved Pin
The N.C. pin between function pins are reserved for compatible and flexible design.</td></tr>
<tr><td>19</td><td>BS1</td><td rowspan="2">I</td><td rowspan="2">Communicating Protocol Select
These pins are MCU interface selection input. See the following table:
<table><tr><th>BS[1:0]</th><th>Bus Interface Selection</th></tr><tr><td>00</td><td>4 line SPI</td></tr><tr><td>01</td><td>3 line SPI</td></tr><tr><td>10</td><td>8-bit 8080 parallel</td></tr><tr><td>11</td><td>8-bit 6800 parallel</td></tr></table>Note
(1) 0 is connected to VSS
(2) 1 is connected to VDD</td></tr>
<tr><td>20</td><td>BS0</td></tr>
<tr><td>21~24</td><td>NC</td><td>P</td><td>Reserved Pin
The N.C. pin between function pins are reserved for compatible and flexible design.</td></tr>
</table>